

Application Form No.

**PROSPECTUS AND APPLICATION
FORM FOR ADMISSION TO
M.Tech (EDT) PART TIME
COURSE**



NIELIT AURANGABAD

**An Autonomous body of Department of Electronics & Information Technology,
Ministry of Communication & Information Technology, Govt of India.**

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1.0 NIELIT Aurangabad –An Introduction:

NIELIT , Aurangabad (the institute) is an unit of NIELIT Society, which is an Autonomous Scientific Society of Department of Information Technology (DIT), Ministry of Communications and Information Technology (MCIT), Govt. of India. The NIELIT Centre (formerly Centre for Electronics Design and Technology of India, (CEDTI)), Aurangabad was established in the year 1986 jointly by DIT, MCIT (erstwhile the Department of Electronics), Govt. of India, Govt. of Maharashtra and Dr. B.A.M. University, Aurangabad (Maharashtra). It has been established primarily to train man-power in electronics design and technology. The institute is engaged in the area of manpower training, product design & development, applied research and consultancy in the field of Information, Electronics, Communication Technology (IECT). One of the objectives of the institute is to disseminate knowledge through ICT (e-learning).

1.1 Location:

Aurangabad is located in Central Maharashtra and has always been a prominent region on the Deccan Plateau. Having been inhabited since the stone age, it has a long artistic and cultural history with various monuments like Bibi-ka-Makbara (replica of Taj Mahal), Panchakki, Daulatabad Fort (Second Capital of the Delhi Sultanate), Tomb of Mughal Emperor “Aurangajeb” and Aurangabad Caves, etc.. Apart from these, the city is surrounded with world heritage sites like Ajanta (101 km) and Ellora (27 km), holy places like Paithan (60km) and Shirdi (120km). The industrial area of the city has developed rapidly and was the fastest growing industrial city. Aurangabad, about 388 km north-east of Mumbai, has Airport and Railway Station and is connected to Mumbai and New Delhi by Air and Rail. Pune is about 230 km and is well connected by road to all major cities.

The institute, having a beautiful building of 12,200 square meters (1,31,272 sq ft) on 18.65 acres of hill side land, is located within the campus of Dr. B. A. M University at a distance of 05 km from the Central Bus Stand and 8 km from the Railway Station.

1.2 Institute’s Thrust Areas:

- Instrumentation / Automation for various sectors with focus on Agriculture Sector or Agri-Electronics.

1.3 Other Areas:

- | | |
|---------------------------------------|--|
| • e- learning | • Embedded System Design |
| • Information Security | • Simulation/VLSI Design |
| • Information Technology & Networking | • Agri-electronics |
| • Opto-electronics | • Multimedia & Animation |
| • Power Electronics | • PCB Design & Fabrication & SMD Facility |
| • Communication English | • Industrial Design, CAD/CAM & Prototype Development |

1.4 Activities:

1.4.1 Academic / Training Programs:

A) Long Term Courses.

- A.1 Doctoral (Ph. D) Program in Engineering & Technology.
- A.2 AICTE approved Long Term Courses.

- Six semesters (3 years) M Tech in Electronics Design and Technology Part Time
- Four semesters (2 years) M Tech in Electronics Design and Technology Full Time
- Eight semesters (4 years) B. Tech in Electronics Engineering
- Six semesters(3 Years) Diploma in Electronics Production and Maintenance (DEPM)

Salient Features

- Autonomous courses under Dr. B.A.M. University, Aurangabad.
- Practical oriented quality education with emphasis on design and project work.

B) Short Term Courses:

The institute is known for its informal Short Term industry oriented Courses (STCs) i.e. computer oriented, design oriented, and specialized, manufacturing technology oriented courses of duration 01-52 weeks to update the knowledge of industry professionals and academicians with latest technology trends. The institute is offering Short Term Courses in the areas of IECT such as Embedded System Design, VLSI Design, Information Security, Information Technology, e-learning, Multimedia and Animation, Industrial Design, CAD/CAM, Opto. Electronics, Power Electronics, PCB Design and Fabrication etc.. Apart from above, the institute has also established laboratory in the area of Agri-Instrumentation & Control, Microprocessor and Micro controller, Analog & Digital Techniques, Consumer Electronics, Test and Measurements, Mechanical Workshop and in allied fields.

Salient Features

- Courses in emerging areas of Electronics, IT and allied fields.
- Application Oriented.
- Part Time and concurrent with on-going studies.
- Targeted towards Industry Professionals and Academicians.
- Custom designed & Modular Courses.
- Corporate Training.

C) Schemes:

The Schemes launched in line with the national policy are as under:-

- Hardware Technical Consultancy Scheme (HTCS) in 1995.
- Software Technical Consultancy Scheme (STCS) in 1996.
- CEDTI Franchise Scheme (CFS) in 1999.
- Certificate Course in Information Technology (CIT) in 1999.
- Bioinformatics Courses in 2003.
- DOEACC Scheme on Hardware Courses jointly with MAIT in 2005.
- Course in Computer Concept (CCC) in the region in 2007.
- ITeS-BPO (Banking) Course jointly with Indian Institute of Banking & Finance (IIBF), Mumbai in 2009 & relaunched in October 2011

- Work related to National Population Register covering Madhya Pradesh, Chattisgarh, and Union Territory initiated from June 2011.
- Project of Capacity building in the areas of Electronic Product Design and Production Technology initiated from July 2012.
- B.Tech.(ESE) and Part Time M.Tech(EDT) launched from July 2013.

Salient Features

- Value addition
- Enhanced employment opportunity.
- Opportunity to learn new hardware & software packages concurrently with ongoing studies/work.
- Centralized examination & certification.
- Quality education to masses at affordable cost.

1.4.2 Industry Interaction:

The institute is also providing the services like product design & development, product engineering, proto-type development, process automation, consultancy, etc. to industries. The institute is also making all efforts to create best infrastructure to provide quality services to industry in servicing and maintenance of sophisticated instruments / machines, support in technology absorption and procurement of latest equipment/ machines.

1.4.3 R & D, Projects and Consultancy:

Post Graduate level academic projects are of one (01) year duration, whereas Diploma level projects are of one (01) semester (six months) duration. Students are encouraged to interact with industry to expose them to industry environment and motivated to undertake real problems of industry as their innovative project work, guided by the faculty. In addition to above, the institute also undertakes Government as well as industry sponsored projects. Some of them are “Training of Teachers in e-learning”, “Information Security Education & Awareness” and Women Empowerment through Value Added Skill Development in IECT”. Apart from above, the consultancy is also provided to the industry.

1.5 Laboratories:

Beautifully landscaped building with 12,200 Sq. meters built-up area, situated on 18.65 acres of hillside land in the university campus, is housing lecture halls and modern facilities along with the following Laboratories:

Embedded System Design	Agri Instrumentation and Control
Simulation/ VLSI Design	Opto electronics
Information Security	Power Electronics
E-learning	PCB Design and Fabrication
Multimedia & Animation	SMD Facility for Low Volume Production
Software Development	Prototype Development
Networking	Mechanical Workshop with Fitting Shop
Industrial Design	Consumer Electronics
CAD/CAM	Test and Measurement
Computers Centre	Analog & Digital
Service & Maintenance	Language (English) Skill

1.6 Other Amenities / Facilities:

Lecture Halls	Placement Cell
Seminar Hall	Cafeteria
Conference Hall	Boy's Hostel
Auditorium	Girls Hostel
Local Area Network with 100 (100 Mbps) Nodes.	
Leased line internet connectivity	Guest House
Comprehensive Library with DELNET Facility & e-Journal accesses (Area -331 sq Mtr, Books – 8273, Bound volumes – 554, Journals – 04 No, Online Access through IEL– 570 IEEE Journals & 1700 ISO Standards, Learning Resources (LRs)–307.	Vehicle Parking
Video conferencing & information (touch screen) kiosk	Stores (172 Sqm)
Uninterrupted Power (63 KVA DG Set Back-up)	Record room (143 Sqm)
Gymnasium(DEPM &M.Tech)	Sports grounds
Karate(DEPM &M.Tech)Female students only	Jogging Track

1.7 Achievements:

- Trained students through AICTE approved courses
- Examined & Certified candidates through various Franchisee/Accredited Schemes & Short Term Courses.
- Trained teachers from schools/colleges/university in “E-learning”.
- Completed NPR Data Digitization, M.P., Chhattisgarh and Dadra ,Nagar, Haveli.
- Conducting „O” level training program for SC/ST Jobseekers at locations in the region, sponsored by DGE&T, New Delhi.
- Excellent response to AICTE approved DEPM, B. Tech (ESE), M. Tech (EDT) Courses. and PhD Program.
- Placement of qualifiers in leading and reputed industries. Employment to the students in leading and reputed organizations like C.G Coral. Lucent India, Texas, L&T, HCL, Wipro Technologies, BITS, IIT, CEDTI, BEL, HAL, ISRO, DRDO, BARC, ECIL, Honeywell Cyrus logic L&T EMSYS.
- Launched „O” and „A” level Courses under DOEACC Scheme on Hardware Courses in 2005.
- Launched ITeS-BPO (Banking) Scheme jointly with IIBF, Mumbai for XIIth qualified youths, particularly from rural India for enhancing their employment opportunities in Sept, 2009.
- Designed and developed Multimedia rich e-content for the CCC Course in Marathi, Tamil and Gujarati language and is being developed in Konkani Language.

- Established Virtual Learning Centre (VLC) and English Communication Lab.
- Design and Developed Electronics Products and Software for industry.
- Completed / in progress the projects, funded by Government of India.
- Provided consultancy and other services to Industry like Bajaj Auto Ltd, Videocon, Sterlite, Siemens, Meltron, Maharashtra Police Wireless, etc.
- Effective utilization of Infrastructure & Manpower to become self sufficient.
- Entered into memorandum of Understanding (MoU) with Indian Institute of Technology (IIT), Mumbai, Marathwada Institute of Technology (MIT), Aurangabad (MS), Indian institute of Banking and Finance (IIBF), Mumbai and Chamber of Marathwada industries and Agriculture (CMIA), Aurangabad (MS) and Shivaji Arts Commerce and Science College, Kannad (MS).
- Organized national level seminar / workshop in Agri-Electronics, Electronics Product Design, Neural Networks, e-learning, etc.
- Implementing the project of Capacity building in the areas of Electronic Product Design and Production Technology initiated from July 2012.
- 86 Candidates have taken admission to this institute as a Research Centre to carry out research leading to Ph.D.

1.8 New Initiatives:

- To initiate work to implement online delivery and examination first for the Scheme, then for the Short Term Courses and finally for formal courses.
- To launch further more Short Term Courses and Schemes in emerging areas of IECT such as in the areas of VLSI Design in e-mode (online) in collaboration with IIT Bombay, Embedded System Design, e-learning, Information Security, CAD/CAM and Industrial Design, etc.
- To launch Post Metric Diploma, Under Graduate and Post Graduate Engineering Programmes in VLSI Design, Embedded System Design, Agri-Electronics, Information Technology and Electronics Design and Technology.
- To prepare and submit project proposals in the area of Skill Development Program, e-learning, Agri-electronics, Opto-electronics, VLSI and Embedded System Design.
- Started full fledged Gymnasium for DEPM, B.Tech and M.Tech. Started B.Tech.(ESE) and Part Time M.Tech(EDT) launched from July 2013.

2.0 Formal Courses :

The Institute Offers:

- a. AICTE approved six semesters (three years) Diploma in Electronics Production and Maintenance (DEPM) to meet the requirement of industry by creating dedicated and immediate productive technicians.
- b. AICTE approved eight semesters (four years) B.Tech in Electronics Engineering.
- c. AICTE approved four semesters (two years) M. Tech in Electronics Design and Technology [M. Tech (EDT)] full time to produce Electronics Design Engineers.
- d. AICTE approved six semesters (three years) M. Tech in Electronics Design and Technology [M. Tech (EDT)] part time to produce Electronics Design Engineers.

These courses are practical oriented and are designed with an emphasis on design and project work. The quality of education is maintained by periodic review and update of syllabus considering the latest trends and needs of industry, in-depth study by the students through semester system, transparent evaluation system and flexibility being autonomy granted to the Centre by Dr.

B.A.M. University, Aurangabad (M.S). The Centre enjoys the reputation of its students getting employed in reputed industries and organizations almost immediately on their completion of courses or settled as successful entrepreneur.

e. Doctoral (Ph.D.) Program in Engineering & Technology.

The Ph.D. Program was launched in July, 2006 to provide quality Human Resource for teaching and research by utilizing facility and expertise available at the institute and its linkage with premier academic institutions and industry. Efforts are being made through this program to inculcate the appropriate attitude, aptitude, knowledge and skills required for the above professions. The quality of education is maintained by rules and regulations for admission, selection of guide, periodic (semester wise) submission of progress report, guidelines for research scholars and guides, etc.

2.1 .0 MASTER OF TECHNOLOGY (ELECTRONICS DESIGN AND TECHNOLOGY) [M.Tech (EDT)] Part Time Course

This is a six semesters (3 years) course, approved by AICTE, New Delhi. Part time course is only for working professionals or for professionals with at least two years of work experience. . His / Her working place should be within 60 km distance from the institute

2.2 .1 Eligibility

a) B.E. / B.Tech Degree or equivalent in Electronics / Electrical / Telecommunication / Computers / Information Technology/ Instrumentation engineering from a recognized University with at least 55% of marks (relaxable to 50% for candidates belonging to SC / ST category). And minimum of two years full time work experience in a registered firm/Company/Industry/Educational and /or Research Institute/any Government Department /Government, Autonomous organizations in the relevant field in which admission is sought.

A letter shall be furnished by employer stating that the candidate is being sponsored to seek admission in the respective course and allowed to attend lectures in the evening as per the institute timetable. The employer should also indicate that the candidate will not be withdrawn midway till the completion of the course.

2.2 .2 Intake 24 (11+7+4+2)

2.2 .3 Categories : (Open/OBC/SC/ST and PD).

2.2 .4 Selection:

Selection of candidates for admission to M.Tech (EDT) Part Time COURSE – (24 seats) will be done by NIELIT Aurangabad

Candidates will be short listed based on the minimum eligibility criteria. **List of Shortlisted Candidates will be displayed on the institute website.**

PI note that the shortlisted candidates will not be informed through post The selection for admission to M. Tech (EDT) program, from the short listed candidates will be based on merit of B.Tech marks and their performance in an interview to be conducted in the month of July . Seats are reserved for candidates belonging to reserved categories as per Government of India Rules and approval of AICTE, New Delhi.

Provisional admission will be granted on the basis of order of merit of the candidate as per the category-wise merit list, prepared on the basis of on merit of B.Tech marks and their performance in an interview As per admission norms, the admission process continues till approved intake of thirty (24) candidates are admitted to the M. Tech Course by operating the category-wise main (selected) list and then, the category-wise waiting list, if required or till a time limit decided by the competent authority is over, whichever earlier. The category-wise main (selected) list and waiting list of the candidates for admission to M. Tech Course will be displayed on the Notice Board of this institute. The date and time for operation of the waiting list shall also be declared along with the list of selected candidates. The selected candidates are required to register on the day as notified by payment of fees & submission of required documents, otherwise their claim shall be forfeited.

All the waitlisted candidates should make themselves available at the time of operation of the waiting list, otherwise their claim shall be forfeited. The waitlisted candidates, available at the time of operation of the waiting list, shall be provisionally admitted as per the merit of the category-wise waiting list. The selected main and waiting list candidates are required to register on the day as notified along with the list displayed by making payment of fees & submission of required documents otherwise their claim shall be forfeited.

Admission process of the M. Tech Course is completed when the approved intake of twenty four (24) candidates are provisionally admitted and registered or a time limit decided by the competent authority is over, whichever is earlier.

2.2 .5 Scheme of Instruction for M. Tech (EDT) Part Time Course:

Every student has to register for the core subjects and electives, relevant to his/her course at the start of the semester in consultation with the concerned faculty /Academic Section.

Semester I (Autumn)

SL No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	M101	Industrial Design of Electronic Equipment	3	0	2	4
3.	M103	Analog & Digital Systems	3	0	2	4
4.		Elective - 1	3	0	2	4
Total			9	0	6	12
ELECTIVES						
M104	Power Electronics					
M105	Instrumentation & Process Control					

Semester II (Spring)

SL No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
2.	M102	Microcomputer System Design	3	0	2	4
4.		Elective - 2	3	0	2	4
5.		Elective - 3	3	0	2	4

Total		09	0	06	12
ELECTIVES					
M106	Medical Electronics I				
M107	Optoelectronics and Optical Fibre Communication Devices				
M108	Software Engineering I				

Semester III (Autumn)

SL No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
2.	M202	Advanced Microcomputer System Design	3	0	2	4
4.		Elective - 4	3	0	2	4
5.		Elective - 5	3	0	2	4
Total			9	0	6	12
ELECTIVES						
M206	Digital Communication					
M207	Optoelectronics and Optical Fibre Communication System					
M208	Software Engineering-II					

Semester IV (Spring)

L No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	M201	Project Feasibility Seminar	2	0	0	2

3.	M203	Physical Design of Electronic Equipment	3	0	2	4
6.		Elective - 6	3	0	2	4
Total			9	0	6	12
ELECTIVES						
M204	Agri Instrumentation & Control					
M205	Advanced Power Electronics					

Semester V (Autumn)

SL No.	COURSE CODE	COURSE TITLE	L	T	P	C
PROJECT WORK						
1.	PROJECT WORK and SEMINAR		0	0	32	16
TOTAL : 16 CREDITS						

Semester VI (Spring)

SL No.	COURSE CODE	COURSE TITLE	L	T	P	C
PROJECT WORK						
1.	PROJECT WORK and SEMINAR		0	0	36	18
TOTAL : 18 CREDITS						

Term Course Load

The electives are selected by the students in consultation with the concerned faculty /Academic Section. During the course period, student has to pass certain number, of subjects and complete satisfactorily the assigned project work .On valid grounds, the authority may advise a student, who is unable to complete the course requirement in the normal period, to continue for an extra term

2.2 .5 Admission in the event of cancellation of secured admission by a student for M. Tech (EDT) Part Time Course and M. Tech (EDT) Full Time course is as follows:

If any vacancy arises due to registered student leaving the course after completion of admission process, the vacancy may be filled on case to case basis at the discretion of the Director till the admission process is completed, by following below mentioned procedure:

- i) The students from the selected candidates, who could not reach the Centre for admission on the pre intimated day because of legal and/or genuine reason(s) and approaching/contacting the institute are first considered for filling the said vacancy. ii) After (i), the candidates, who have not been offered the admission and approaching, shall be considered.
- iii) Then the candidates, as per order of merit shall be informed about the offering of the admission.

2.2 .6 Assessment

A student is permitted to appear for the semester examination subject to he or she has a minimum attendance of 70% in theory and practical classes, completes all his/her sessional assignments and clears all his/her dues. Non appearance in any examination is treated as the student having secured zero mark in that subject examination. Every subject has credit points based on the hours of study required. Every student is awarded Grade points out of maximum 8 points in each subject .Based on the Grade Points obtained in each subject. Semester Grade Point Average (SGPA) and then Cumulative Grade Point Average (CGPA) are computed. For computation of SGPA and CGPA, refer ANNEXURE – IV

2.2 .8 Assistance in Placement:

The placement cell of the Centre offers all assistance to the students interested in securing an appropriate employment. The students of this course have been well received by the Industries.

2.2 .9 Tuition and Other Fees:

Sr.No	Fee Head	Fee structure	
		Category of Student	
		National	International
01	Cost of Prospectus cum Application Form	NIL	NIL
02	Admission/Eligibility	Rs.1500/- (One time)	Rs. 7500/- (One Time)
03	Tuition Fee per semester	Rs. 18750/-**	Rs. 93750/-
04	Development fee per semester	Rs.5000/-**	Rs.25000/-
05	Examination Fee per semester	Rs.1250/-**	Rs.6250/-
06	Interest Free Caution Deposit for Library	Rs.1250/-* (one time)	Rs.6250/-* (one time)
07	Other Facility Fee (connectivity etc.) per semester	Rs.1500/-	Rs.7500/-
08	Laboratory Fee per semester	Rs.2500/-	Rs.12500/-
09	Library Fee per semester	Rs.875/-	Rs.4375/-
10	Cultural, Welfare Fee etc. per semester	Rs.1000/-	Rs.5000/-
11	Sports fee	Rs.1875/-	Rs.9375/-
	Total fee to be paid at start of admission	Rs 35500/-	Rs 177500/-
12	Project Fee(for III Sem and IV sem.)	Rs.3750/-	Rs.18750/-
13	Backlog Examination Fee per paper	Rs.625/-	Rs.3125/-
Hostel Related fees			
14	Hostel Fees per semester	Rs.7500/-	Rs.37500/-
15	Interest Free Caution Deposit for Hostel	Rs.2500/-* (one time)	Rs.12500/-* (one time)
16	Interest Free Mess Deposit	Rs.2500/-* (one time)	Rs.12500/-* (one time)
	Total Hostel Fees	Rs 12500/-	Rs.62500/-
17	Late Fee Fine:-		

1. Fee Paid upto Due Date.	Nil	Nil
2. 1st week after Due Date.	Rs.100/-	Rs.500/-
3. 2nd week after Due Date.	Rs.200/-	Rs.1000/-
4. 3rd & 4th week after Due Date.	Rs.500/-	Rs.2500/-
5 5th week after Due Date.	Rs 1500/-(Admission fee)+Rs 500/-	Rs 7500/-(Admission fee)+Rs 2500/-

* The deposits are one time payment and to be paid at the time of admission and returnable on the completion of the course without interest.

** Not to be taken from SC/ST Candidates.

There shall be a 10% increase in the fee every year(Sr No 3,4,5,8,14)

2.11 Other Information

The student is required to submit the undertaking to agree to abide by the terms and conditions of the institute and AICTE New Delhi at the time of admission in the prescribed format. The students are encouraged to interact with industry for getting exposed to industry environment and provided all support and motivated to select & undertake real problems of industry as their project.

All the students (boarders as well as day scholars) are required to strictly abide by the rules of the Institute / Centre, failing which disciplinary action may be taken against them. Mess –Canteen facility is compulsory for the students who reside in NIELIT Hostel. Students staying in hostel will have to pay Advance Payment towards mess facility for the semester along with regular fees .Approximate amount toward mess/ canteen will be Rs 15,000/-for six months

3.0 Reservation:

As per Government of India's Policy, there is a reservation for persons, belonging to socially and educationally backward classes and persons with Disabilities (Physically handicapped Persons) in education. All reservations are not of the same nature. There are two types of reservations, which may, for the sake of convenience, be referred to as "Vertical Reservations" and "Horizontal Reservations". The reservations in favor of

Scheduled Caste, Scheduled Tribes and Other Backward Classes (OBCs), made under article 16 (4), may be called vertical reservations whereas reservations in favor of physically handicapped, made under clause (1) of Article 16, can be referred as horizontal reservations. Horizontal reservations cut across the vertical reservations, called as "interlocking reservation".

3.1 Percentage of Reservation:

As per Government of India Rules, there is a reservation of 27% of seats for the OBCs, 15% of seats for the SCs and 7.5% of seats for the STs in admission to various courses. Further there is a reservation of at least 3% of seats for Persons with Disabilities (PDs),

as per section 39 of the Persons with Disabilities (Equal opportunities, Protection of Rights and Full Participation) Act, 1995. Since the reservation for PDs, the horizontal reservations, is interlocking reservations, the candidate selected against this quota will be placed in the appropriate category, that is, if the candidate belongs to SC/ST category, he/she will be placed in that quota by making necessary adjustment. Similarly, if the candidate belongs to General/Open category, he/she will be placed in that category/quota by making necessary adjustment.

3.2 Relaxation in Qualifying Marks:

The relaxation in qualifying marks is 5% for SC & ST candidates only.

4.0 How to Apply

Applications for admission to M.Tech (EDT) Part Time COURSE – 2016, along with necessary enclosures must reach NIELIT Aurangabad, Dr. B.A.M. University Campus, Aurangabad-431 004 (MS) by July 15, 2016. Application form can be downloaded from website .The selection for admission to M.Tech (EDT) Part Time COURSE – 2016, from the short listed candidates will be based on merit of B.Tech marks and their performance in an interview to be conducted on July 28, 2016 10.am onwards. List of Shortlisted Candidates will be displayed on the institute website.

Cost of the Prospectus cum Application form for is Rs. 500/- (Rs.300/- for SC/ST/PD candidate. Demand draft of of any Nationalized bank drawn in favor of NIELIT, Aurangabad, payable at Aurangabad (Maharashtra), must be enclosed along with the filled in application form. Candidate belonging to SC/ST/OBC/PD category must substantiate their claim by enclosing proof for the same.

The filled in application form along with relevant documents should reach NIELIT Aurangabad, Dr Babasaheb Ambedkar Marathwada University Campus ,Aurangabad 431004, in due date ie 15 July 2016. Incomplete and late received application forms will not be considered. NIELIT Aurangabad will not be responsible for any postal delay. The decision of Director, NIELIT, Aurangabad in respect of selection and closing of admission will be final. Canvassing in any form will lead to disqualification. Candidates will appear for interview at their own expenses.

5.0 Prohibition and Ban on Ragging:

As per the directions of the Hon^{ble} Supreme Court in SLP No. 24295 of 2006 dated 16-05-2007 and in Civil Appeal number 887 of 2009, dated 08-05-2009, ragging is strictly prohibited and banned. Any student found guilty in act of ragging will be punished as per government rules and regulations. Therefore all the students are advised to keep away from ragging and promote good and healthy environment in the institute. Students are further advised to be disciplined; sincere and punctual and promote peace and harmony. They are encouraged to participate in cultural and sports activities.

6.0 Refund of fees in the event of cancellation of admission:

6.1 There will be no refund :

6.1.1 If admission is cancelled after the 1st Semester is over.

6.1.2 If admission is cancelled in 1st Semester and the vacancy is not filled.

6.2 If admission is cancelled in 1st Semester and the vacancy is filled, then Refund of fees will be as under :-

a) No refund of admission fee.

b) Student leaving before start of the course – Entire fee less Rs1000/- (Rupees One Thousand only) as processing fee.

c) Student leaving after start of the course but before closing of admissions- Entire fee less Rs1000/- or (Semester fee/6), whichever is higher.

d) Student leaving after start of the course and closing of admissions-Entire fee less [Rs1000/- + (Semester fee/6)x Registered Period in number of months]

5.3 For students staying in hostel,[(Hostel Rent/6)x Registered Period in number of months]will be deducted from Semester hostel fee paid by the student.

5.4 All the deposits will be refunded after adjusting student dues, if any.

5.5 Fraction of month will be treated as full month.

7.0 List of Faculty & Technical Staff Members:

S.No	Name of the Employee	Designation	QUALIFICATION
1	Shri. John G.	Director/ Scientist/Engineer 'F'	ME(Power Systems) , B.E(Electrical Enggineering)
2	Shri S.T. Valunekar	Scientist/Engineer 'E'	B.E, (Ele. Engg) M. Tech. (Ele.)
3	Shri Sasidharan P T	Scientist/Engineer 'E'	B. Tech (Electronics and Communications), M.E (Aerospace Engg)
4	Shri Gera Sasikumar	Scientist/Engineer 'E'	B.Tech,(Mechanical Engg), M.Tech (Mechanical Engg)
5	Shri Y.P. Gogia	Scientist/Engineer 'D'	M.Sc (Phys. With Electronics), M.Tech (Opto Electronics)
6	Shri D.S. Raje	Scientist/Engineer 'C'	B.E (Electronics)
7	Smt. W.S.Kandlikar	Scientist/Engineer 'C'	B.E. (Electronics) M.E. (Electronics)
8	Shri Lakshman K	Sceintist/Engineer 'C'	B.Tech, (Computer Science), M.Tech. (Computer Science)
9	Shri A.K.Joshi	Principal Tech. Officer	M.Sc. Physics, (with an Electronics as special subject)
10	Shri Y.A.Khan	Principal Programmer	B.Sc, MCA
11	Smt. P.D. Bharne	Principal Tech. Officer	DETC (Elect & Telecom)
12	Shri Th. Sunil Kumar Singh	Principal Tech. Officer	BE(ECE)

13	Smt. V. S. Jahagirdar	Sr. Technical Officer	B.E. (Elect) M.Sc. (App. Sci) M.Tech (EDT)
14	Shri M.S. Kshirsagar	Sr. Technical Officer	DIEE
15	Shri M.L.Garud	Sr. Technical Officer	DIE
16	Shri K.S. Choudhari	Sr. Technical Officer	2 years ITI Radio/ADIT, 'O' Level, ADIT from CEDTI
17	Shri M.S.Chavan	Asst. Library & Info. Officer	B.Sc., B.Lib. Sci. M.Sc. M. Lib. Sci.
18	Shri S.G.Wankhede	Technical Officer	ITI, DEPM
19	Smt. S.R.Jape	Technical Officer	B.F Art
20	Shri M.K. Kulkarni	Technical Officer (W/s)	ITI (Fitter), NCTVT
21	Shri M.Mahto	Technical Officer	DEE
22	Shri S.S. Paropkari	Sr. Electrician SGII	ITI (Electrician)
23	Shri R.N.Potbhare	Technical Officer (W/s)	ITI (Welder)

Administrative Staff

24	Shri Vijay Chandola	Deputy Director (F)	B.Com, CA (Inter), ICWA (Inter)
25	Shri Ajaysimhan V K	Assistant Director (P&A)	BA (English) Certificate Course in Database Management
26	Sr. S.T.Hosurkar	Senior Private Secretary	B.A
27	Shri Sunil Agawane	Finance Officer	B.Com.
28	Shri U.M. Joshi	P.S.	B.A
29	Shri A.K.Zade	Senior Asstt.	B.Com, GDMM
30	Shri R.D.Muley	Jr. Asstt.	S.S.C
31	Shri Maruti Hamand	Jr. Asstt.	S.S.C
32	Shri P.A. Sonne	Attedent	9th Pass
33	Shri B.D. Tigote	Attedent	9th Pass
34	Shri Vasant Jadhav	Safaiwala	9th Pass

Application Form No:_____

NIELIT , Aurangabad, Maharashtra (India)

ANNEXURE-I

Application Form for M.Tech Part Time Course

To,
The Director,
NIELIT Centre, Aurangabad Dr
B .A.M.University Campus,
Aurangabad
4310004 (MS) India

Important Instructions

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. No refund of Fees..

Sir/Madam,

I hereby apply for grant of eligibility to **M.Tech (EDT) Part Time Course** during the Academic year -----and request you to kindly grant me a certificate of eligibility to the said course in NIELIT Centre,Aurangabad, Maharashtra (India). I submit my particular as under:



CANDIDATE'S DETAILS

Last Name : _____

First Name : _____

Middle Name : _____

Mother Name : _____

Date of Birth : DD/MM/ YYYY **Nationality:** **Sex:**

Category(OPEN/SC/ST/OBC and PD)

ADDRESS FOR CORRESPONDENCE															
E-mail ID:	Tel No. (with ISD/STD)														
Mobile No															

ACADEMIC QUALIFICATIONS (in ascending order)						
Sr. No.	Examination Passed	Name of School / College	Name of Examining body (Board / University)	Year of Passing	% Marks obtained	Class / Division

WORKING EXPERIENCE	
Name of Industry / Institution	
Type of Work / Experience	
Experience in years	

Name and address of the School / College / Institution last attended:

PAYMENT DETAILS										
Mode of Payment (Tick Appropriate)										
Cash	<input type="checkbox"/>	Cash Receipt No.		Receipt Date	D	D	M	M	Y	Y
Demand Draft	<input type="checkbox"/>	DD No.		DD Date	D	D	M	M	Y	Y
Bank Name:										

Check List (Please attach Attested photocopies of all the Certificates for Sr. No. 2 to 6 below)			
01.	One Passport Size Photograph pasted & attested	YES	NO
02	Date of Birth proof, copy attached (Xth Mark-sheet / Certificate)	YES	NO
03	Cast (OBC/SC/ST) Certificate, copy attached	YES	NO
04	Disability Certificate for PD, copy attached	YES	NO
05	Graduation Passing Certificate, copy attached	YES	NO
06	Studentship Letter, Original attached	YES	NO
07	Qualifying Degree Certificate, copy attached	YES	NO
08	GATE Score Card, copy attached (M.Tech(EDT) Full Time Course	YES	NO
09	Sponsorship letter Original / Experience letter attached	YES	NO
10	No Objection Certificate For M.Tech (Edt) Part Time Course		
11	Payment (in the form of DD / Cash Receipt) Enclosed	YES	NO
Note: The application form is liable to be rejected if found incomplete or if the necessary & appropriate documents & Payment are not enclosed along with.			
CANDIDATE SIGNATURE:			

FOR OFFICE USE ONLY:

Registration No						Application Form No					
-----------------	--	--	--	--	--	---------------------	--	--	--	--	--

Date: DD /MM /YYYY Checked By: AAO-I (Acad)

Acad. Co-ordinator

ANNEXURE-II

(To be typed on Rs. 100/- Stamp Paper)

Declaration and Undertaking

I hereby declare that I have carefully read this application form for eligibility and admission and have noted the instructions / requirements thereby. I have also carefully noted the rules of eligibility & conduct and discipline, laid down by the NIELIT Centre, Aurangabad and I agree to abide by them. I understand and declare that I shall be responsible for any discrepancies, error, wrong or incorrect information, supplied by me in this application form and for cancellation of admission thereby or otherwise found ineligible. I undertake to furnish the necessary certificate(s) / document(s) / paper(s) in original alongwith a true copy of each of them as and when asked for, failing which I understand that my eligibility and admission stands automatically cancelled and that the NIELIT Centre, Aurangabad is not responsible for the same.

I hereby declare that the information furnished by me in this form is true and correct to the best of my knowledge. I am liable to be disqualified if the competent authority notices that I have furnished any false information.

Yours Faithfully,

(Name & Signature of Student)

Date:- DD/MM/YY

Place:-

ANNEXURE- III

Computation of SPGA and CGPA

The overall performance of a student is evaluated by assigning equal weightage to all the semesters four in case of. M.Tech, full time and Six in case of M.Tech part time) in order to maintain the quality of education.

A student is permitted to appear for the semester examination, subject to he or she has a minimum attendance of 70% in theory and practical classes, completes all his/her sessional assignments and clears all his/her dues. Non appearance in any examination is treated as the student having secured zero mark in that subject examination.

The evaluation is based on a average weightage system. Every subject has credit points based on the hours of study required. Every student is assessed in a subject with equal weightage to sessional work and semester examination, thereby making the students study regularly.

Every student is awarded points out of maximum 8 points in each subject. (based on 8 Points Scale). Based on the Grade points obtained in each subject the Semester Grade Point Average (SGPA) and then Cumulative Grade Point Average (CGPA) are computed. The computation of SGPA & CGPA is as under:

Semester Grade Point Average (SGPA) is the weighted average of Grade Points obtained by a student in a semester and is computed as follows:

$$SGPA = \frac{U_1 \times M_1 + U_2 \times M_2 + \dots + U_n \times M_n}{U_1 + U_2 + \dots + U_n}$$

Where U_1, U_2, \dots are subject credit of the respective course and M_1, M_2, \dots are the marks obtained in the respective subject out of 8 (Grade Point)

The Semester Grade Point Average (SGPA) for all the six semester is also mentioned at the end of every semester.

The Cumulative Grade Point Average (CGPA) is used to describe the overall performance of a student in the Diploma / Degree course and is computed as under:

$$CGPA = \frac{\sum_{n=0}^{6/4} SGPA(n) C_n}{\sum_{n=0}^{6/4} C_n}$$

Where $SGPA(n)$ is the n^{th} Semester SGPA of the student and C_n is the n^{th} Semester total credit. The maximum value of n is six (6) in case of DEPM Program and four (4) in case of M. Tech Program.

The Semester and Cumulative GPA are rounded off to the second place of decimal.

ANNEXURE- IV

Format of Caste Certificate (For SC/ST Candidates Only)

The authorities empowered to issue this certificate are:

1. District Magistrate/ Additional District Magistrate / Collector / Deputy Commissioner / Addl. Deputy Commissioner / Deputy Collector / Class I Stipendiary Magistrate / Sub-Divisional Magistrate / Taluka Magistrate / Extra Asst. Commissioner (not below the rank of Ist class stipendiary Magistrate) Chief Presidency Magistrate / Addl. Chief Presidency Magistrate / Presidency Magistrate Revenue Officer not below the rank of Tahsildar. Sub-divisional officer of the area, where the candidate and / or his family normally resides. Administrator / Secretary to Administrator / Development Officer at Lakshadweep Islands.

This is to certify that

Mr./Ms. _____

son/daughter/wife of _____ of Village / Taluka
district _____ state _____ and ordinarily residing at
Village / Taluka _____ district _____
state _____,
an _____ applicant appearing for M. Tech.(EDT) course of
NIELIT , Aurangabad, belongs to Scheduled Caste/Tribe.

Signature

Name & Designation

Office Seal

Date:-

Place:-

*(Note: The above certificate format is suggestive only. The candidate is required to
prepare and submit it on separate sheet.)*

ANNEXURE- V

Format of Caste Certificate (For OBC Candidates Only)

This is to certify that Shri/Smt./Kum. _____
Son/Daughter of Shri/Smt. _____ of _____
Village/Town _____
District/Division _____
in the State of _____
belongs to the _____
Community, which is recognized as a backward class under:

- (i) Resolution No. 12011/68/93-BCC(C) dated 10/09/93 published in the Gazette of India Extraordinary Part I Section I No. 186 dated 13/09/93.
- (ii) Resolution No. 12011/09/94-BCC dated 19/10/94 published in the Gazette of India Extraordinary Part I Section I No. 163 dated 20/10/94.
- (iii) Resolution No. 12011/7/95-BCC dated 24/05/95 published in the Gazette of India Extraordinary Part I Section I No. 88 dated 25/05/95.
- (iv) Resolution No. 12011/96/94 BCC-dated 09/03/96.
- (v) Resolution No. 12011/44/96 BCC dated 06/12/96 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 1/12/96.
- (vi) Resolution No. 12011/13/97- BCC dated 03/12/97.
- (vii) Resolution No. 12011/99/94- BCC dated 11/12/97.
- (viii) Resolution No. 12011/68/98-BCC dated 27/10/99.
- (ix) Resolution No. 12011/88/98-BCC dated 06/12/99 published in the Gazette of India Extraordinary Part I Section I No. 270 dated 06/12/99.
- (x) Resolution No. 12011/36/99-BCC dated 04/04/2000 published in the Gazette of India Extraordinary Part I Section I No. 71 dated 04/04/2000.
- (xi) Resolution No. 12011/44/99-BCC dated 21/09/2000 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 21/09/2000. (xii) Resolution No. 12016/09/2000-BCC dated 06/09/2001.
- (xiii) Resolution No. 12011/01/2001-BCC dated 19/06/2003.
- (xiv) Resolution No. 12011/04/2002-BCC dated 13/01/2004.

(xv) Resolution No. 12011/09/2004-BCC dated 16/01/2006 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 16/01/2006.

Shri./Smt./Kum. _____

and/or his family ordinarily reside(s) in the _____

District/Division of the State of _____

This is also to certify that he/she does not belong to the persons/sections (Creamy Layer) mentioned in Column 3 of the Schedule to the Government of India, Department of Personnel & Training O. M. No. 36012/22/93-Estt.(SCT) dated 08/09/93, which is modified vide OM No. 36033/3/2004 Estt. (Res.) dated 09/03/2004.

District Magistrate/
Deputy Commissioner, etc.

Dated:

Seal

Place:

NOTE:

- (a) The term „Ordinarily“ used here will have the same meaning as in Section 20 of the Representation of the People Act, 1950.
- (b) The authorities competent to issue Caste Certificates are indicated below:
 - (i) District Magistrate /Additional Magistrate / Collector / Deputy Commissioner / Additional Deputy Commissioner / Deputy Collector / Ist Class Stipendiary Magistrate / Sub-Divisional Magistrate / Taluka Magistrate / Executive Magistrate/ Extra Assistant Commissioner (not below the rank of Ist Class Stipendiary Magistrate).
 - (ii) Chief Presidency Magistrate / Additional Chief Presidency Magistrate / Presidency Magistrate.
 - (iii) Revenue Officer not below the rank of Tahsildar and
 - (iv) Sub-Divisional Officer of the area, where the candidate and / or his family resides.

ANNEXURE- VI

Declaration/Undertaking – for OBC Candidates only

I, _____ son/daughter of _____ resident of village/town/city _____
_____ district _____ in the State of _____ hereby
declare that I belong to the _____ community _____ which is recognized as a backward class by
the Government of India for the purpose of reservation in services as per orders contained
in Department of Personnel and Training Office Memorandum No. 36012/22/93-Estt.
(SCT), dated 8/9/1993. It is also declared that I do not belong to persons/sections (Creamy
Layer) mentioned in Column 3 of the Schedule to the above referred Office Memorandum, dated
8/9/1993, which is modified vide Department of Personnel and Training Office Memorandum No.
36033/3/2004 Estt. (Res.) dated 9/3/2004

Signature of the Candidate

Place:

Date:

NB : Declaration/undertaking not signed by Candidate will be rejected

Creamy Layer Definition

OBC Creamy layer is defined comprehensively at <http://ncbc.nic.in/html/creamyayer.html> All candidates for the OBC reserved seats should make sure that they do not satisfy any of the creamy layer criteria as listed in the website. Some general exclusion for quick reference (no way comprehensive) are as follows.

1. Any one of the parents holds a constitutional position in Govt. of India.
2. Any one of the parents is a class I Officer.
3. Both the parents are class II officers.
4. Any one of the parents is employed in an equivalent rank to class I officer or both parents equivalent to class II officer in a public sector, insurance companies, banks, universities or in other organizations.
5. Land holding on irrigated land is 85% or more of the statutory ceiling area
6. Parents income is more than Rs. 4.5 Lakhs per year

ANNEXURE- VII

Format of Physical Disability Certificate (For Physically Handicapped Candidates)

Certificate No. _____

Date: _____

This is to certify that I have examined Mr./Ms. _

Son/Daughter/Wife of Mr. _____ age _____
of the Candidate

Sex _____ On _____

Photograph

(i) He/She is suffering from _____
(name of the physical disability),

which comes under the sub category Blindness /Low vision/Speech & Hearing impairment/Orthopaedic disorder/Learning disabilities. Dyslexia, Dyscalcula, Dysgraphica, Spastic.

(ii) The percentage of disability is _____ %.

(iii) The disability is permanent in nature.

(iv) This condition is progressive/non-progressive/likely to improve/not likely to improve. Reassessment of this case is not recommended/is recommended after a period of _____ years _____ months.

(v) The candidate is capable of carrying out all activities related to theory and practical work as applicable to Diploma/Postgraduate Course in Electronics Engineering & IT without any special concession and exemptions.

(vi) This certificate is issued as per the provisions given in the Persons with Disability Act, 1995 and its amendment.

This Certificate is issued for the purpose of his/her admission to first year of M. Tech. (EDT) part time course for the Academic Year 2014-15 at NIELIT Centre, Aurangabad (MS).

Director

All India Institute of Physically
Handicapped, Mumbai
OR Dean/Civil Surgeon

Seal of Institution/Hospital

Date:

Place:

ANNEXURE- VIII
Sponsorship Certificate (For sponsored candidates only)

(On letterhead of the Institute /Organization)

Outward No.:

Date :

To
The Director
NIELIT Centre (Formerly DOEACC),
University Campus,
Aurangabad 431 004

Sub: Sponsoring of an employee for M Tech (EDT) full time Course.

Sir,
I/We hereby sponsor the candidature of Mr. / Ms.----- son/daughter/wife of----- ,
an employee of our organization, for undergoing M Tech course in Electronics Design and
Technology at your institute as Full Time. He/ She is working with our organization since as-----
-----as a (designation)-----

The candidate is sponsored without project

☐

The candidate is sponsored with a project.

☐

The Project title is

We will abide to the guidelines/rules framed by NIELIT Centre , Aurangabad for the purpose of sponsorship of the candidate.

In case he/she is selected for the course, we shall bear the entire expenses of his/her studies.

We shall relieve him/her of his/her duties for the entire period of his/her M Tech Course/ to enable him/her to devote full time to the studies

Yours faithfully

Signature

Name & Designation

Seal of Sponsoring Organization

Note: 1. Prepare the Sponsorship Certificate on the company letterhead as per the format given above.

2. Please read the guidelines before preparing the sponsorship certificate.

3. Please tick appropriate box (In case box is not properly ticked, the application *is liable to be rejected*).

ANNEXURE- IX

**NO OBJECTION CERTIFICATE FOR M. Tech (EDT) PART TIME
CANDIDATE**

(On letterhead of the Institute /Organization)

Outward No.:

Date :

To,
The Director
NIELIT (Formerly DOEACC Centre)Aurangabad
Dr B A M University Campus,
Aurangabad 431 004

This is to certify that Mr. / Msis serving
in our Organization / Institution as.....
.since.....

The Organization / Institution has no objection for the mentioned candidate to join the
M.Tech(EDT) Part time Course at NIELIT , Aurangabad and will permit the candidate to
attend lectures in the evening as per the institute timetable.

The Organization / Institution will render all possible help to him / her in persuation of studies.
He / She will be relieved for a requisite period, if selected for the course.

His / Her working place is within 60 km distance from the institute. If candidate resigns or
transferred beyond 60 km distance, the intimation in this regard will be given to your office in
writing within a week.

Signature of Competent Authority

Name :

Designation:

Seal of Sponsoring Organization

ANNEXURE - X

Guidelines for Sponsorship

Before sponsoring the candidate for the M. Tech course, the employer/sponsoror is required to carefully read the guidelines, mentioned below, for the issue of Sponsorship Certificate. Application with incomplete Sponsorship Certificate is liable to be rejected.

1. Candidate sponsored along with a project shall be given preference.
2. Sponsorship is meant to help industry/institution by way of sponsoring the candidate, who can pursue the project of their importance at NIELIT Centre, Aurangabad (MS)
3. Industry/Institution sponsoring the candidate with project is required to deposit the advance of Rs. 10,000/- (Rupees Ten Thousand only) per candidate, sponsored for pursuing the project. The industry also required to bear total expenditure incurred on the project. Similarly the Industry/Institute is required to reimburse all the expenditure incurred by the NIELIT Centre, Aurangabad on account of the project of their sponsored candidate; otherwise the result of the final examination along with the project of the sponsored candidate will be withheld.
4. It will be responsibility of the candidate to complete the project in all respect as required by his/her sponsoring industry.
5. The candidate is required to work on the project till sponsoring industry and NIELIT Centre, Aurangabad are satisfied.
6. The project is jointly evaluated by both industry and NIELIT Centre, Aurangabad.
7. Industry is required to issue a certificate / letter of regarding acceptability of the project. Based on this acceptability, the exam of the candidate is conducted.
8. Sponsoring Industry will receive the prototype developed by the candidate, if industry bears the total expenditure incurred on the project. Right of the intellectual property generated by the candidate will be the property of NIELIT Centre, Aurangabad.

ANNEXURE- XI

IMPORTANT DATES TO REMEMBER

Sr. No.	ACTIVITY for Candidates of M.Tech(EDT) Part Time Course	Important Dates
1	Last date for receipt of filled in application form	15th July, 2016
2	Interview	28th July, 2016
3	Declaration of list of selected & waitlisted Candidates	28 th July, 2016
4	Declaration of list of selected & waitlisted Candidates	28 th July, 2016
5	Registration	29 th July, 2016
6	Academic calendar starts	1 st August 2016

