

## FACILITIES AVAILABLE

### HARDWARE

- Xilinx FPGA Development Boards (Virtex 6, Spartan 6, Virtex 2, Spartan3, Spartan2 etc)
- Altera Development Boards (Stratix II, Cyclone IV, III, II etc)
- PSoC Development Boards from Cypress Semiconductor
- Ultrasound Transmitter, Receiver Boards.
- Logic Analyzer, CROs, Power Supplies, Testing Equipments SMD Soldering Station.



### SOFTWARE

- Mentor Graphics
- Cadence Tool Suite
- MATLAB
- Synopsys ASIC Tool Suite
- Xilinx ISE
- Altera Quartus
- PSoC Creator

### FACILITIES PROPOSED

FPGA trainer Kits

## ACTIVITIES

### FORMAL PROGRAMS

M.Tech (ES) - Advanced Engineering Mathematics, Multimedia Compression Techniques, Advanced Digital System Design, Information Security

### COURSE WORK OFFERED

M.Tech (EDT) - Advanced Mathematics, Multimedia Compression Techniques, Advanced Digital System Design, Information Security

### TRAINING

### PROGRAMS

- All India PG Diploma in VLSI & Embedded Hardware Design-AVL500
- All India PG Diploma in ASIC Design & Verification-AVL600
- SC/ST Training Programmes - O Level
- IEEE Workshop/FDPs/Corporate Training Programmes

## R&D ACTIVITIES/PROJECTS(ONGOING/COMPLETED)

- **Ultrasound Scanner Prototype** in association with Niranjana Ultrasound Pvt Ltd
- **IP Cores Developed in-house - 08**

## ACHIEVEMENTS/HONORS/AWARDS/PAPERS PUBLISHED

Total Papers Published by Group - 25

### MAN POWER

- K.S Lalmohan      Sci/Eng 'E'- Group Head
- Jayaraj U Kidav    Sci/Eng 'C'
- R.Nandakumar     Sci/Eng 'C'
- M.Soumini         Sr Tech Officer
- Sreejeesh S.G      Tech Officer

