

# National Institute of Electronics and Information Technology Gorakhpur

**Name of Group:** Electronics Division

**Name of Course:** DIGITAL SYSTEM DESIGN using VHDL

**Objective:**

1. To introduce VLSI Design Cycle
2. To have a knowledge of HDL language and Digital Circuit Design
3. VHDL Coding Cycle
4. Digital Circuit Design and its simulation using VHDL

**Duration:** 4 Weeks / 6 Weeks

**Eligibility:** 3 Year Diploma/B.E/B.Tech/M.Tech in  
Electronics/Electrical/Instrumentation/Computer Science

**Course Fees (Proposed):** Rs. 1000/- per week (+ GST)

**Registration Process:** Candidates have to apply in prescribed application form. The forms can be collected from NIELIT Gorakhpur centre or can be downloaded from the NIELIT Gorakhpur website. The duly filled form along with the course fees has to be submitted at NIELIT Gorakhpur centre. The Fees deposited is Non-Refundable.

## **Course Content:**

<b>Modules:</b>	<b>Duration</b>	<b>Contents</b>
Module 1	4 Week	Digital System Design Using VHDL
Module 2	2 Week	Project: N-BIT ALU DESIGN

- \* There will be 3 Hours Session per day.
- \* These sessions will include 1- Hour Theory Class and 2 Hours Practical.

**Mode of Payment:** Fees can be paid either by swiping debit/credit card or by challan.

For any queries and more details please contact Sh. A.G. Rao (**8317093870**)