

**NATIONAL INSTITUTE OF ELECTRONICS AND
INFORMATION TECHNOLOGY**

(A Centre of NIELIT, New Delhi)

(An Autonomous Scientific Society of Dept. of Electronics & Information Technology,
Ministry of Communications & IT, Govt. of India)

P.B. NO.5, NIT Campus P.O., CALICUT-673 601, KERALA

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LIMITED TENDER ENQUIRY

Ref. No. 2(1050)/2016-17/VLSI/ICDU/EVM

Date: 30.05.2016

DUE DATE: 13-06-2016

1. Quotations are invited for the supply of the items as per Annexure.
2. The quotations duly **SIGNED**, SEALED AND SUPERSCRIBED ON THE ENVELOPE WITH THE REFERENCE NO. AND DUE DATE, should be addressed to the undersigned so as to reach him on or before the due date stipulated above. Quotations received after the due date will not be considered.
3. The quotations should be valid for acceptance for a period of **sixty days** from the due date and the time required for delivery should also be clearly indicated.
4. Quotations will not be accepted by Fax, Email or any such electronic data transfer form.
5. The quotations should be for goods exactly conforming to our requirements and specifications.
6. If the item is under DGS&D Rate contract, the number and the price applicable must be mentioned. It may also please be indicated whether the supply can be made direct to us at the **DGS&D Rate Contract Price**.
7. Relevant literature pertaining to the items quoted with full specifications and drawings, if any should be sent along with the quotations, wherever applicable. Samples, if called for, should be submitted free of charges and collected back at the supplier's expenses.
8. Copy of Manufacturing licence, Principal or Authorized Distributor/Dealer Certificate, and Proprietary Certificate, as applicable, should be enclosed.
9. Quotations should be for **free delivery** at our Centre and should clearly specify the **delivery period**. If delivery quoted is Ex-Godown/Consignor Station, delivery charges consisting of freight, packing & forwarding charges, insurance etc. should be indicated separately. Goods should be supplied duly carriage paid and insured.
10. Sales Tax Forms "**C or D**" **will not be issued to parties**. CST may be charged at the concessional rates applicable for purchase of scientific goods for educational institutions.
11. Security Deposit @5% of the Purchase Order/Invoice value shall be retained, in case order/contract value exceeds ₹1 lakh, which will be released after the expiry of warranty period.
12. Goods shall not be supplied without an official purchase order.
13. Payment : Payment will be made after completion of supply, installation/assembly and commissioning of the items covered by the order along with necessary spares supplied to the entire satisfaction of NIELIT, CALICUT. Payment against invoices shall normally be made within 30 days of receipt and acceptance of equipment/materials at our office. **No advance payment will be made under any circumstance.**
14. Incomplete quotations and quotations which do not comply with all the above instructions are liable to be summarily rejected.
15. NIELIT, CALICUT does not bind itself to accept the lowest or any such quotation and has the right to accept or reject whole or any part of tenders or a portion of the supply of goods without assigning any reasons. No correspondence in case of rejected tenders will be entertained.
16. Earnest Money Deposit (E.M.D.) for ₹7,750/- shall be sent along with the quotation by way of DD in favour of Executive Director, National Institute of Electronics and Information Technology, Calicut, drawn on State Bank of India, CREC Calicut Branch, NIT Campus, Chathamangalam, Calicut – 673 601, failing which the quotation will be rejected.

Yours faithfully,

Purchase In-charge
For Executive Director

Encl: Annexure

ANNEXURE TO ENQUIRY

Our Quotation No. 2(1050)/2016-17/VLSI/ICDU/EVM

Date : 30.05.2016

1	Name of item(s)#	1) AFE5818 EVM - 01 No. 2) TSW1400 EVM - 01 No. 3) TX - SDK-V2 Ultrasound Transmit Solution Evaluation Board - 01 No.
2	<u>Specifications</u>	(As per detailed specifications sheet attached)
3	Price per unit in Rs. (in Figures & words)	Attach separate list, if required.
4	Total Price in Rupees (in figures & words)	
5	Delivery Period	
6	Terms of Delivery	
7	Taxes, Duties, Octroi & Any other statutory levies or charges	
8	Transportation, Insurance, Packing & Forwarding etc	
9	Discount/off etc. if any	
10	Payment Terms	
11	Validity of Tender	
12	Warranty	
13	Any other remarks (Please specify make)	
14	Signature of the Tenderer with Name and Date	
15	Address, Email & Mobile No.	
16	Central Public Procurement Portal (www.eprocure.gov.in) Registration, Email login ID	

NB: (1) In cases where the supply involves both Sales Tax and Service Tax, the quotation should indicate separately the break-up of Sales Tax and Service Tax applicable to the respective components/service. It may be noted that dual taxation, i.e., charging both Sales Tax as well as Service Tax to the same component or service (in respect of software etc) is not allowed. **The prices quoted should be Academic/Educational Prices wherever applicable.**

(2) Enquiry for the above items and specifications can also be downloaded from our website www.calicut.nielit.in or www.eprocure.gov.in.

(3) **Please register at www.eprocure.gov.in and intimate login details without fail. Watch websites for regular updates.**

SPECIFICATIONS OF AFE 5818 EVM

- 16-Channel Complete Analog Front-End
- Programmable Gain Low-Noise Amplifier (LNA)
- 40 dB Low Noise Voltage Controlled Attenuator
- 14-bit Analog to Digital Converter
- AFE 5818 EVM
- Mini-USB-Cable
- Power cable with barrel connector

SPECIFICATIONS OF TSW1400 EVM

- Complete 16-bit data capture and pattern generation platform for TI's high speed ADC's and DAC's
- Compatible with LVDS-, CMOS-* interfaces
- Analysis on up to 8 simultaneous channels
- 1 GB memory, enabling 512M sample depth
- Support up to 1.5 GSPS LVDS I/O rates for both ADCs and DACs
- Compatible with software for TSW1405 and TSW1406 GUI software
- Altera Stratix IV FPGA based design module
- Integrates functions of TSW1200EVM and TSW3100EVM legacy solutions
- 1 USB
- Power Adaptor suitable for 230V,50Hz

SPECIFICATIONS OF TX-SDK V2

- 1 Ultrasound Transmit V2 Solution Evaluation Board
- 1 Set of 8 SAMTEC RF Connector-to-SMA bulkhead cables
- 1 Supply Voltage Cable with Phoenix Connector and 12 labeled Banana Plugs for:
 - +1.8V
 - +3.3V
 - ±5V
 - ±10V
 - ±40V
 - ±50V
 - VSUB
 - GND
- 1 Opal Kelly XEM3001 FPGA Module
- 1 Opal Kelly-to-Evaluation Board Adapter & Ribbon Cable
- RF connector port x 8 numbers