

राष्ट्रीयइलेक्ट्रॉनिकीएवंसूचनाप्रौद्योगिकीसंस्थान,कालीकट National Institute of Electronics & Information Technology, Calicut



Ministry of Electronics and Information Technology
Government of India

On-line Courses Prospectus

ISO 9001-2015 Certified

Electronic Hardware Design and Board Bring up using Allegro/ OrCAD 17.2

Date: 25th August – 27th August 2021 (2 hrs daily)

Time: 11.00AM to 1.00PM

Course Description

This course provides step by step guidance on how to start with Electronic Board (PCB) design, its constraints and the methodologies for bringing up the board The Course is planned with instructor led live sessions of lectures and demonstration of Allegro/OrCAD 17.2 EDA tool.

March 2020 |

National Institute of Electronics and Information Technology, Calicut

CDS/CA/7.5.1/F 40/R13

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Who can attend?

Any under graduate/ diploma student/ working professional/ industry people with basic knowledge of Electronics and PCB Design.

- · Students pursuing Engineering,
- Professionals working on Electronic Hardware design s development activities and others who would like to develop competency in Electronic Hardware system.
- Research students
- Teaching professionals& faculties

Course Content

Session	Topic	Date
1	Basics of Hardware Design, History of PCB, Types & Fabrication of PCB, Electronic Components and Packages	25/08/2021
2	Schematic Entry in Allegro/ Orcad capture 17.2, Creation PAD, Footprint etc.	26/08/2021
3	Net list File Generation, Layout Editor, Gerber file Generation. Board Bring up methodologies.	27/08/2021

Prerequisite

- Knowledge of Electronic components and packages.
- Knowledge of Basic Electronic circuits.

Fee and important dates

Fee	Rs. 750/- (Including GST)
Last date for registration &	24 th August 2021
payment	

Sharing of Link / passwords & other details

25th August 2021

Certificate

Certificate will be issued to all candidates who complete the program.

Minimum Requirements

- Attendees must have a computer system with a good internet connection.
- Computer System Configuration Windows OS 7/8/10, i3/i5, 64bit processor, Minimum 4GB RAM / Mobile -Android
- Link to attend the course shall be forwarded on 25-08-2021

Faculty Profile

Qualification and experience of the faculty handling the sessions

1. Manoj Kumar MK – Senior Technical Officer, 27 Years of Experience (7 years Industry Research & Development)

Course Coordinator

Manoj Kumar M K

Senior Technical Officer 9447433175(M), 0495 2287266- Exn-219(O)

Email: manojkumar@nielit.gov.in

Terms and Conditions

- 1. In case any registered candidate could not attend the online session due to technical issue at their side there will not be any refund of the course fee and the sessions will not be repeated.
- 2. In case the online course is cancelled /postponed due to some technical issue at NIELIT side and new date is not convenient to the candidate, our liability is limited to the refund of the course fee and NIELIT shall not be responsible for any consequential damages.

For more details about our institution and facilities visit us

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Autonomous Scientific Society of Ministry of Electronics & Information Technology, Govt. of India

NIELIT Calicut

Post Box No. 5, P. O. NIT Campus, CALICUT – 673601, Kerala



Tel.: 0495-2287266 Mob.: 9446011266

Email: trng@calicut.nielit.in
URL: http://nielit.gov.in/calicut

