Date: 30th August 2021

Course Description:

This is an online, instructor – led certificate program which provides a thorough knowledge about the Vega Processors, its programming, Linux porting on the processors and interfacing for internet of things (IoT) applications. Swadeshi processor are **M**ade in **I**ndia processors which will be the future processors used across world for the Internet of Things applications. This course will enhance your skills for using these swadeshi processor made by CDAC Trivandrum for the IoT Applications.

Program Objectives

To learn, Practice the Linux Porting on the Vega Processors for various applications. To get exposure in industry standard methodologies from the makers of the processors.

Who can attend?

Students of Engineering (UG & PG) & MSc (Electronics), PhD scholars, faculty members and professionals from Industry/Startups**.**

Duration

- Proposed length of the training: 5 Days. **10 Hours lecture 15 Hours practicals.**

Course Fee
INR 1500/- For Students INR 3,000/- For Faculty INR 5,000/- For Industry/Corporate Last date for payment and confirmation: 26th August 2021

REGISTER NOW

Payment Guidelines: -

Online fund transfer can be made via your Internet Banking, Google Pay to the following account and proof of the same has to be uploaded during the registration.

Account details:

Name of the Institute: National Institute of Electronics and Information Technology, Calicut.
Account Holder: Director NIELIT Calicut

Account No: 10401158037 Bank Name: SBI, NIT Chathamangalam

IFSC No: SBIN0002207 MICR Code: 673002012

For any queries WhatsApp to 9447769756, Please don't call, we will reply to you at the earliest.

Delivery Mode: Online. Live classes followed by online assignments over LMS. Students should have Laptop/PC with high speed internet connectivity.

Tentative Schedule

Duration		1 week
Tentative Timings		10 am to 12.00 noon (Theory) Lab/Assignments can be submitted online on Leaning management Systems (Any Time)
Tentative dates		30th August 2021 to 3rd September 2021

		Syllabus	
Day	Session -1 10.00 AM – 11.30 PM	Session 2 11.45 – 1.15 PM	Remote Lab Duration : 2 Hours
Mon	Overview of RISC V & Swadeshi Processor Architecture (**CDAC**)	Embedded Linux overview (**NIELIT**)	Remote Lab Familiarisation (**NIELIT**)
Tue	Overview of Buildings Custom Embedded Linux (**NIELIT**)		Remote Lab (**NIELIT**)
Wed	Building Custom Linux for Vega & Device access -1 **CDAC**		Remote Lab (**NIELIT**)
Thu	Building Custom Linux for Vega & Device access -2 **CDAC**		Remote Lab (**NIELIT**)
Fri	Mini Project (**NIELIT** & **CDAC**)		Online Assessment(**NIELIT**)

<u>Certificate</u>: e-Certificate will be mailed to the registered email address after completion of the course.

Course Materials

Lectures Notes will be made available in the LMS

<u>**Coordinators**</u>

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