

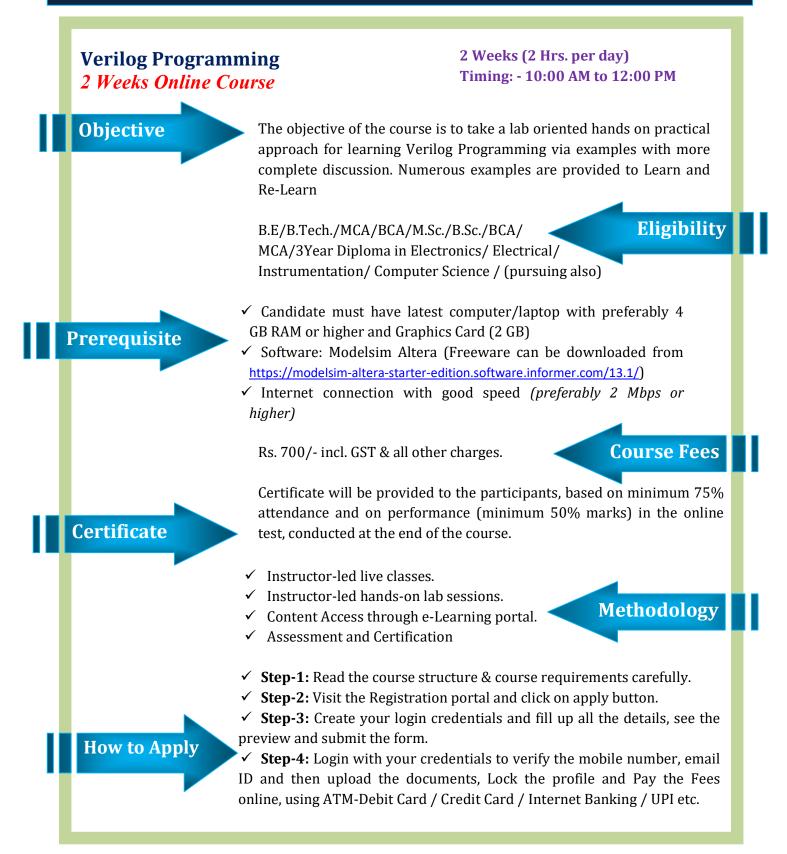
National Institute of Electronics & Information Technology

Gorakhpur – Extension Centre Lucknow

(Under Ministry of Electronics and Information Technology, Govt. of India) MMMUT Campus, Deoria Road, Gorakhpur-273010 http://www.nielit.gov.in/gorakhpur/



## Verilog Programming





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## **Course Content**

Day	Торіс	Day	Торіс
Day#01	Introduction to Verilog	Day#06	GENERATE Statement CONDITIONAL Statement
Day#02	"Always" Block for Beginners	Day#07	CASE Statement CASE GENRATE Statement
Day#03	All Modeling Styles	Day#08	TEST BENCH
Day#04	Verilog Data Types	Day#09	FSM in Verilog
Day#05	Verilog Operators	Day#10	Mealy and Moore State Machine Design

## **Course Coordinator**

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