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## WALK IN INTERVIEW

NIELIT Aurangabad an institute under NIELIT New Delhi, (An autonomous Scientific Society under Dept. of Electronics and Information Technology, Ministry of Communication and IT, New Delhi), under the project "Capacity Building in the area of Electronics Product Design & Production Technology", is conducting Walk in Interview for various positions equivalent to Associate Professor, Assistant Professor, Lecturer, Lab Assistant, Project Assistant, Account Assistant, Office Assistant and Steno on consolidated pay. The short listed candidates will be engaged on contract basis for a period of 6 months, which may be further extended based on performance and coterminous with the project.

Candidate possessing the relevant qualification and experience according to AICTE norms in the field of Micro Controller, Embedded System, Product Design, FPGA, VLSI and other allied fields of electronics for faculty positions and relevant qualification and experience for non faculty positions are invited to appear for the interview.

16 June 2014 (Forenoon) - Submission of Bio data

16 June 2014 (Afternoon) - Interview for faculty positions

17 June 2014 - Interview for non faculty positions

at the Conference Hall of the centre.

Candidate shall come along with the necessary original certificates, one set of xerox copies and Demand Draft of Rs 250/- in favour of **Director**, **NIELIT Aurangabad**, Payable at Aurangabad (Maharashtra).