

National Institute of Electronics & Information Technology, Aurangabad (Maharashtra)



(An Autonomous Institution Under Dept. of Electronics & IT, Govt. of India)

Town of the implication						
Type of the institute:	Government Funded Technical Institutions					
Complete Mailing Address:	Dr. B.A.M. University Campus, Aurangabad - Maharashtra					
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About the Institute:

National Institute of Electronics & Information Technology (NIELIT), an Autonomous Scientific Society under the administrative control of Department of Electronics & Information Technology (DeitY), Ministry of Communications and Information Technology, Government of India, was set up to carry out Human Resource Development and related activities in the area of Information, Electronics & Communications Technology (IECT). NIELIT has endeavoured to establish standards to be the country's premier institution for Examination and Certification in the field of IECT. It is also one of the National Examination Body, which accredits institutes/organizations for conducting courses in IT in the non-formal sector.

Over the last two decades, NIELIT has acquired very good expertise in Non-formal training programs in Information Technology, Information Security, ITeS-BPO, Computer Hardware Maintenance, Bio-Informatics, Electronic System Design & Manufacturing, Industrial electronics, Consumer Electronics, Electronic Product Design, Industrial Automation, Office Automation, Telecom, Instrumentation, IT&ITES, Media& Entertainment(Animation), Network Infrastructure & Management etc, besides high end courses offered by NIELIT Centres at Post-Graduate level (M.Tech) in Electronics Design & Technology, Embedded Systems etc.

The basket of activities of NIELIT is further augmented by the wide range of projects that it undertakes. NIELIT has demonstrated its capability and capacity to undertake R&D projects, consultancy services, turnkey projects in office automation, software development etc.

NIELIT is also the nodal implementing agency on behalf of DeitY for Data Digitization of the population of 15 assigned States and 2 Union Territories for the creation of National Population Register (NPR) project of Registrar General of India (RGI).

NIELIT is also successfully executing the Agriculture Census and Input Survey project under which tabulation of about 10 core data records have to be done. NIELIT has planned a road map for adopting appropriate pedagogy for metamorphosing NIELIT into an Institute of National Importance.

NIELIT Aurangabad is engaged both in Formal & Non-Formal Education in the area of IECT besides development of industry oriented short term training programmes for enhancing knowledge and skill in the area of Electronics Product Design and Production Technology, Embedded system, VLSI, Agri Electronics, Consumer Electronics, Industrial Electronics, Industrial Automation, Communication Technology, IT& Networking, CAD/CAM etc. A gap exist between academic qualifier from educational institution and professional qualifiers required for an industry. An academic qualification involves the study of a subject with an academic discipline and (hopefully) research focus. The overriding purpose of this qualification is a contribution to the learners specialized knowledge of a subject and not necessarily the application thereof. But the industry need not only understanding but also practical experience to the learner to enable the learner to apply the knowledge in a practical manner, in a professional practice. This obviously leads to a completely different set of skills, each with different purposes and contexts for the world of work. The NIELIT Aurangabad Centre (formerly Centre for Electronics Design and Technology of India, (CEDTI)), Aurangabad was established in the year 1986 jointly by Deity, MCIT (erstwhile the Department of Electronics), Govt. of India, Govt. of Maharashtra and Dr. B.A.M. University, Aurangabad (Maharashtra) to bridge this gap as one of the objective.

The employment opportunities in electronic industry is estimated to grow phenomenally. To cater the demand for skilled manpower required for the electronics design and manufacturing sectors, is aimed through various formal programs. The objective of AICTE approved M.Tech, B.Tech & DEPM program (with Autonomous status granted by Dr.B.A.M. University, Aurangabad), is generation manpower not only with knowledge and understanding but also practical experience, to enable the learner to apply the knowledge in a practical manner, in a professional practice, as required by electronics design and manufacturing industry to meet the country's needs and serve the international market. The quality of education is maintained by periodic review and update of syllabus considering the latest trends and needs of industry, transparent evaluation system and flexibility being autonomy granted to the Centre by Dr.B.A.M. University, Aurangabad (M.S).

B. Tech in Electronics System Engineering

The syllabus for this program is framed by an expert committee of academicians, technocrats and industrialist to impart the learners with required Core Competence, (Solid foundation in mathematical, Scientific and Engineering & Technology fundamentals required to solve real life design problems), Breadth(Breadth of scientific and Engineering & Technology knowledge and skill to comprehend, analyze, design & create novel electronic products and solutions for real life), Professionalism (inculcating professional/ethical attitude, effective team work skills, multidisciplinary approach) to excel in electronics design and manufacturing industry.

The core courses of Electronic Systems Engineering stream focuses on integration of analog & digital electronics hardware and software, computers, and communication technologies to varieties of systems such as embedded systems, instrumentation and control, consumer electronics, telecommunications, and power and energy etc. to innovate tomorrow's intelligent electronic products and systems today. Electronics System Engineering is an interdisciplinary field deals with how to design and manage complex electronic systems over their life cycles. Issues such as requirements engineering, industrial design, product engineering, ergonomics, aesthetics, system-level packaging, thermal design, reliability, EMI&EMC, testing and evaluation, maintainability, serviceability and many other disciplines necessary for successful system development, design, implementation, and ultimate disposal after decommission. It overlaps technical and human-centered disciplines such as industrial engineering, control engineering, software engineering, organizational studies, and project & Design management etc. The essentials of Electronic Systems Engineering is dealt in core subjects and others as electives.

B.Te	ch Fee Structure:						
Sr.	Fee Head	Category of Student					
No	гее пеаа	National	International				
01	Admission/Eligibility	Rs.1500/-(One	Rs. 7500/-(One				
		time)	Time)				
02	Tuition Fee per semester	Rs. 17500/-**	Rs. 87500/-				
03	Development fee per semester	Rs.3750/-**	Rs.18750/-				
04	Examination Fee per semester	Rs.625/-**	Rs.3125/-				
05	Project Fee(for VII Sem & VIII sem.)	Rs.2500/-	Rs.12500/-				
06	Backlog Examination Fee per paper	Rs.625/-	Rs.3125/-				
07	Hostel Fees per semester	Rs.7500/-	Rs.37500/-				
80	Interest Free Caution Deposit for	Rs.1250/-*	Rs.6250/-				
	Library	(one time)	(one time)				
09	Interest Free Caution Deposit for	Rs.2500/-*	Rs.12500/-				
	Hostel	(one time)	(one time)				
10	Other Facility Fee (connectivity	Rs.1500/-	Rs.7500/-				
	etc.)per semester						
11	Interest Free Mess Deposit	Rs.2500/-(one	Rs.12500/-(one				
		time)*	time)				
12	Laboratory Fee per semester	Rs.1875/-	Rs.9375/-				
13	Library Fee per semester	Rs.750/-	Rs.3750/-				
14	Book Bank Fee	Rs. 750/-	Rs. 3750/-				
15	Cultural, Welfare Fee etc. per	Rs.1000/-	Rs.5000/-				
	semester						
16	Sports fee	Rs.1875/-	Rs.9375/-				
17	Late Fee Fine:-						
	a. Fee Paid upto Due Date.	Nil	Nil				
	b. 1st week after Due Date.	Rs.100/-	Rs.500/-				
	c. 2nd week after Due Date.	Rs.200/-	Rs.1000/-				
	d. 3rd & 4th week after Due Date.	Rs.500/-	Rs.2500/-				
	e. 5th week after Due Date.	Rs 1500/-	Rs 2500/-				
		(Admission fee)+Rs	(Admission				
		500/-	fee)+Rs 500/-				

There will be 10% increase in the fee every successive year to the previous year(Sr No 2,3,4,7,12). Refund for cancellation of admission shall be subjected to AICTE rules time to time.

^{*} The deposits are onetime payment and to be paid at the time of admission and returnable on the completion of the course without interest.

^{**} SC/ST Candidates are exempted from this Fees.

Faculty:			
Name	Designation	Educational Qualification	Experience
John .G	Director ; Scientist/Engineer 'F'	BSc Engg, ME	30 years of R&D, Design, Engineering in industry & post graduate teaching
S.T. Valunjkar	Dean (formal Programs); Scientist/Engineer 'E'	B.E, M. Tech.	32 years of R&D, PE industry & post graduate teaching
P.T. Sasidharan	Dean (Non-formal Programs); Scientist /Engineer 'E'	B.Tech, ME.	23 years of R&D industry & post graduate teaching
Gera Sasikumar	Head of CAD/CAM, Scientist/Engineer 'E'	B.Tech, M. Tech.	22 years in industrial consultancy & post graduate teaching inCAD/CAM/Mechenical Engineering
Y.P. Gogia	Head of ICT, Scientist/Engineer 'D'	M.Sc, M. Tech.	26 years in R&D and post graduate teaching
Warsha .Kandlikar	M.Tech Co- ordinator, Scientist/Engineer 'C'	B.E,ME	25 years of post graduate & undergraduate teaching
Deepak Raje	Scientist/Engineer 'C'	BE	25 years of Teaching
Deepak Vasan	Joint Director	M.Sc. Physics	27 years in project management
Lakshman K	Head of MCC Scientist/Engineer 'C'	B.E,ME	9 years of R&D, post graduate & undergraduate teaching
V. S. Jahagirdar	Senior Tech. Officer	M.Tech(EDT)	26 years of post graduate & undergraduate teaching
A.K Joshi	B.Tech /DEPM Co- ordinator Principal Tech.Officer	M.Sc. Physics	29 years of undergraduate teaching
Sunil K. Singh T .	Principal Tech.Officer	B.E	26 years of undergraduate teaching
Y A KHAN	Principal Programmer	M.C.A	27 years of Teaching
Shrddha Choudhary	Sr .Project faculty	M.Tech	VLSI and Embedded systems
Archana salunke	Sr .Project faculty	M.Tech	3 years in IT company and teaching
B1		cal Support Fac	
Name	Designation	Educational Qualification	Experience
P.D.Bharne	Pr TechOfficer	DEDE(E&T)	27 years in PCB
K.S. Choudhary	Sr TechOfficer	BCA	21 years in IP Network
M. S. Kshirsagar	Sr TechOfficer	DIE (Indl. Elect)	28 years Computer Hardware & Microprocessor

M. L. Garud	Sr TechOfficer	DIEE (Indl. Elect	27 years in Power Electronics
S. G. Wankhede	TechOfficer	DEPM	28 years in Consumer Electronics lab
S. R. Jape	TechOfficer	B. F. A. (App. Art)	27 years In fine arts and Product Design
M. Mahto	Sr Tech Assistant	DEE (Elect. Engg.)	25 years In Test and measurement lab
M. K. Kulkarni	TechOfficer(WS)	ITI (Fitter)	26 years in Mechanical Work Shop
R. N. Potbhare	TechOfficer (WS)	ITI (Welder)	20 years in Mechanical Work Shop
S. S. Paropkari	Sr. Electrician	ITI (electrician)	28 years in Power Electronics Lab

R & D Projects and Consultancy

Post Graduate level academic projects are of one (01) year duration, whereas Diploma level projects are of one (01) semester (six months) duration. Graduate level mini project is in the 5th semester and final year projects is in 6th and 7th semester along with other academic works. Students are encouraged to interact with industry to expose them to industry environment and motivated to undertake real problems of industry as their innovative project work, guided by the faculty. In addition to above, the institute also undertakes Government as well as industry sponsored projects. Some of them are "Training of Teachers in e-learning", "Women Empowerment through Value Added Skill Development in IECT". Apart from above, the consultancy is also provided to the industry. List of recent project undertaken by the centre from various ministries is given below.

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SI.	. Project Title							
No.								
1	IT SKILLS Training to Rural Youth of SC/ST &Minority community							
2	Electronics Equipment repair & Maintenance, Training to Rural Youth of							
	SC/ST & Minority community							
3	ITES-BPO Training to Rural Youth of SC/ST &Minority community							
4	Setting up Model career centre at NIELIT Aurangabad							
5	Information Security Education & Awareness (ISEA) Project Phase – I							
6	Capacity Building in the Area of Electronics Product Design and Production							
	Technology							
7	Information Security Education & Awareness (ISEA) Project Phase – II							
8	Electronic System Design & Manufacturing(All India)							
9	Basic Computer Course (BCC) for Panchayathi Raj Institution functionaries							
10	National Population Register Project							

Facilities						
Departments:						
Embedded system & VLSI	IT& Networking					
·						
Electronic Product Design	lectronic Product Design Industrial					
	Automation	Technology				
Agri &Consumer Electronics	CAD/CAM	Basic science				
	&Workshop	&Humanities				
	Labs:					
Embedded system Lab	VLSI Lab	Computer Center				
Electronics & Communication	Power Electronics	s Software				
Lab	Lab	Development Lab				
Product Design(Industrial	Opto Electronics	Networking &IT				
Design)	Lab	Security Lab				
PCB Design, Fabrication & SMD	Consumer	Research Lab				
	Electronics Lab					
Agri Instrumentation & Process	CAD/CAM Lab	Information Science				
control Lab		(Library)				
Testing &Measurement Lab	Workshop					
Other An	nenities / Fact	ilities				
Lecture Halls		Guest House				
Seminar Hall		Cafeteria				
Conference Hall with Video						
conferencing		Boys' Hostel				
Auditorium		Girls' Hostel				
LAN with 100 (100 Mbps) Nodes,						
Connectivity)	Mess (@affordable rate)					
Smart Virtual Class rooms	Jogging Track					
Digital Library with DELNET Fa	Gymnasium					
accesses	Karate (for female					
Online Access through IEL – 570 I	students only)					
1700 ISO Standards, Learning Re						
307						
(Books – 13109, Bound volumes						

Sports grounds Vehicle Parking

Ĵournals etc)

Uninterrupted Power (63 KVA DG Set Back-up)

ACADEMIC BLOCKS



The institute, having a beautiful building of 12,200 square meters (1,31,272 sqft) on 18.65 acres of hill side land, is located within the campus of Dr. B. A. M University , Aurangabad. campus is self-sustained and is equipped with requisite amenities to meet the needs of institute such as classrooms of different sizes, laboratories, library, Air conditioned conference hall, administrative block, medical room and a seminar halls.

LECTURE HALLS



The Institute possess neoteric classrooms, lecture halls and conference hall. Each one of them is well equipped with modern facilities such as Multimedia Projector, LAN & Wi-Fi connectivity, etc., which go along well with today's digital age of teaching. It is made sure that the students get an atmosphere which is comfortable, enhances learning and foster their young minds.

CONSUMER ELECTRONICS LAB





EMBEDDED SYSTEM LAB





INFORMATION SCIENCE (LIBRARY)



The Library of the NIELIT Aurangabad is a continuous growing organism and Its core objective is to support learners, researchers and scientists with its well managed information resources housed in two floors of the library with a total area of 331 Sq.metre. The library has a rich collection of resources in the form of books, journals, ejournals, theses and other documents covering major subject areas like Electronics, Computer Science, Embedded systems, Bioinformatics, Information Security, E-Learning, Agriculture, Bio technology, Control Engineering, Networking, Communication, etc. The collection is organized according to the Dewey Decimal Classification scheme. Open access system is followed in the library. The library is computerized using the software, SOUL developed by INFLIBNET.

Library Resources; Books: 13119, E-books: Subscribed at Books 24* 7

E Journals: Science Direct, IEEExplore Digital Library, ASTM Digital Library etc

Students life at Institute

Institute believes in nurturing talent and provides with unique course curriculum & Learning Environment of academic excellence, leadership, ethical guidelines and life-long learning needed for a long/successful career in industry. Hands - on training is an integral part of curriculum at NIELIT. Strong and equal emphasis is laid on academic, co-curricular and extra-curricular activities to ensure an all-round development of the student while providing multiple platforms for students to improve their soft skills which are imperative for one to excel in his/her domain. NIELIT transforms the students into efficient and effective human being both at personal and professional level. Every year a batch of around 72 students get admitted to the B.Tech program.

1st Year B,Tech admission for students from India is through JEE mains, for details www.josaa.nic.in

1st Year B,Tech Admission for students from abroad is through DASA, for details www.dasanit.org

M.Tech, B.Tech Lateral entry & Diploma in Electronics Production and Maintenance (DEPM) admission is through DTE Maharashtra, for details http://www.dtemaharashtra.gov.in.

Now NIELIT Aurangabad is opening admission for 24th batch of **M.Tech in Electronics Design Technology** with 30 students of intake capacity, 30th batch of **Diploma in Electronics Production and Management** with 60 students of intake capacity and 4th batch of **B.Tech in Electronics System Engineering** with 60 students of intake capacity. it is home to elite students, from the very first batch, NIELIT Aurangabad has seen high rankers of JEE Mains preferring it over even the other new NITs. The institute stands well in both academic and cultural activities. Over the years we have seen a good number of students ranked less than 25,000 in JEE Mains taking admission in the institute. National Institute of Electronics & Information Technology Aurangabad, like all other NITs in India, enrolls students from all the 29 States and 7 Union Territories. Every year, students are selected into the Institute based on their JEE Mains Rank (Formerly AIEEE Rank), and the category they belong to 12 seats have been allocated for students under Direct Admission for Students Abroad (DASA).

Financial Assistance

Scheduled Caste & Tribe students are exempted from paying Tuition fee, Development fee & Examination fee in all semesters only once (i.e. repetition of exam/semester due to any reason the fee is not exempted) under the scheme of Scheduled Caste Sub Plan (SCSP) and Tribal Sub Plan (TSP) of Deity, GoI. For fee exemption necessary certificate as per GOI order to be produced from time to time. We encourage students to apply for scholarship from different organisations.

Training and Placement

Model Career Centre(MCC)

The Directorate General of Employment and Training (DGE&T) Under Ministry of Labour & Employment (MoLE) is implementing the National Career Service (NCS) which aims to provide a variety of employment related services. Under this programme, DGE&T set up a Model Career Centre (MCC) in this centre. MCC will connect all our students with all possible job opportunities in a transparent and effective manner through the use of technology as well as through counseling and training. MCC is aiming at organizing job fairs, counseling fairs, placement drives, for all passing out students and industrial tie-up with the centre. It is also the Training and placement cell of the institute through which students can enjoy lectures, discussion sessions, on-campus conferences, conducted by eminent alumni and current and emeriti faculty.

The MCC also facilitates internships for students in industries, corporate, academia, NGO and organizations of National and International repute. Our students got internships in some of the best MNCs and public sector organizations. A batch of 80 students interned at more than 20 organizations/ companies. Industrial training and internship offer students an opportunity to get experience of the working environment before they graduate. This helps them in correlating the knowledge gained from their curriculum to the insights gained from working in the industry and also motivates them to enhance their skills. Students also understand the bigger picture of working in industrial/ corporate environment and understand the diversified skill requirement. All of this helps them to enhance their technical, interpersonal, managerial and leadership skills. Moreover, students get a chance to plan their professional career. This exposure will help them to deliver their best when they join the industry as working professionals.

Industry and Alumni Relations Industry Interaction

The institute is also providing the services like product design & development, product engineering, proto-type development, process automation, consultancy, etc. to industries. The institute is also making all efforts to create best infrastructure to provide quality services to industry in servicing and maintenance of sophisticated instruments / machines, support in technology absorption and procurement of latest equipment/ machines.

Alumni Association

Alumni Association creates and maintains a life-long connection between the Institute and its alumni. The Alumni Association works to connect alumni, support students and build an unforgettable Institute experience through a diversity of events, programming and services. The mission of the Association is to foster strong bonds between alumni, students and the Institute, to keep alumni informed, and create a network enabling them to remain engaged with their alma mater and help shape it's future through the Association's programmes and services. Ist batch of DEPM celebrated their 25th year of passing out in August 2015 . For 2nd batch their 25th year of passing out is on 13th August 2016

Recreational & Extra Curricular Activities Beyond Academics

We lays great emphasis on holistic development of students. The Institute provides a range of opportunities outside the classroom as well - be it industry interaction for project work, cultural activities, sports competitions, entrepreneurial pursuits, socially relevant activities through NSS etc.

To enhance the student's confidence, leadership qualities, attitude, management, communication and creative skills, the institute encourages a variety of activities. NIELIT Aurangabad organized its first Annual Technical festival ELE-CHROME-2015 in March 2015 which was attended by students from all over Maharashtra.

We offers students opportunities to interact with and learn from corporate professionals, Leaders of the business, representatives of government and non-government organizations, artists and intellectuals regularly visit our campus on invitation to deliver talks and presentations that provide insights to the careers and personal attributes of these role-models.

Apart from that, in order to provide students an exposure of the outside world, a number of technical events like seminars and workshops have been conducted. Clubs are an important part of the co-curricular sphere of NIELITA. Various clubs give students an opportunity to exercise their extra-academic skills and to keep with the tradition of engineering. This initiative also provides the student community with an opportunity to exercise their autonomy in a responsible manner.

Sports & Games

Institute is having well equipped Gym, large green spaces, jogging track, Football/Hockey, Cricket, Volleyball, Badminton and Basket ball etc. NIELIT proximity to SPORTS AUTHORITY OF INDIA gives the ample opportunity for training, round the year, in various sports and games. NIELIT was able to prove its mettle when the students were able to bag medals at inter college sports meet. The institute also hosts its annual sports event KHELO NIELIT, in which all the departments of the institute actively participate to claim the shining trophy.

Art & Culture

In order to cope up with the academic rigour, equal emphasis is put on cultural activities. A healthy campus life plays a pivotal role in the all-round development of students. Along with the healthy academic schedule and the brain-storming class hours, the students of NIELIT immerse themselves in various extra-curricular activities. Hailing from diverse social and cultural backgrounds, they manage various clubs, events and festivals, which build ability to work in teams, endow them with leadership qualities and make them all-rounder's. Institute also organizes Foundation day Program, Poster Presentation, Good governance Day, Ganpati Mahotsav, Debate, Essay writing, Paper Reading Competitions, Group Discussion Competitions, Hindi Pakhwada etc.

Location and Accessibility

Aurangabad is located in Central Maharashtra and has always been a prominent region on the Deccan Plateau. Having been inhabited since the stone age, it has a long artistic and cultural history with various monuments like Bibika Makbara (replica of Taj Mahal), Panchakki, Daulatabad Fort (Second Capital of the Delhi Sultanate), Tomb of Mughal Emperor "Aurangajeb" and Aurangabad Caves, etc..Apart from these, the city is surrounded with world heritage sites like Ajanta (101 km) and Ellora (27 km), holy places like Paithan (60km) and Shirdi (120km). The industrial area of the city has developed rapidly and was the fastest growing industrial city.

Aurangabad, about 388 km north- east of Mumbai, has Airport and Railway Station and is connected to Mumbai and New Delhi by Air and Rail. Pune is about 230 km and is well connected by road to all major cities. The institute, having a beautiful building of 12,200 square meters (1,31,272 sqft) on 18.65 acres of hill side land, is located within the campus of Dr. B. A. M University at a distance of 05 km from the Central Bus Stand and 8 km from the Railway Station.

Academic Program-wise Seats Breakup											
Academic Program Code	Academic Program Name	OPEN	OPEN- PwD	sc	SC- PwD	ST	ST- PwD	OBC- NCL	OBC- NCL- PwD	Total	State/All India Seats
4137	B.Tech in Electronics System Engineering	29	1	9	0	5	0	15	1	60	All India
	Total Seats	29	1	9	0	5	0	15	1	60	