Admission Brochure

for

2015-16 batch M. Tech Courses

in

Embedded Systems (ES)

&

Electronics Design Technology (EDT)

of

NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION TECHNOLOGY

(NIELIT, CALICUT)

An Autonomous Scientific Society of Department of Electronics and Information Technology,
Ministry of Communications & Information Technology, Govt of India.

PB No. 5, NIT Campus P.O, Calicut – 673 601 Kerala.

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Website: calicut.nielit.gov.in

Contents

About Us: National Institute of Electronics and Information Technology (NIELIT), Calicut is an autonomous body of Department of Electronics and Information Technology (DeitY), Ministry of Communications & Information Technology (MCIT), Government of India. This institute was established in 1989 and is located adjacent to the National Institute of Technology Calicut, about 20 km east of Calicut city. The Institute received assistance from UNDP (United Nations Development Program) and was also a participating institution under Project IMPACT of the World Bank and Swiss Development Co-operation for infrastructure development and faculty training.

The Institute has state-of-the-art laboratories, fully equipped with the latest systems and development tools in the areas of **Embedded Systems, VLSI Design, Information Technology, Power Electronics, Product Design, Control & Instrumentation and CAD/CAM**.

The Institute has highly qualified and best available talents in the fields, as its faculty who have undergone specialized training in various International Universities and Industries in USA, UK, Germany, Netherlands etc. and with many years of experience in Embedded Systems. This includes post graduate and graduate engineers having ample teaching and industrial/R&D experience and highly skilled technical support staff to assist the faculty and students in their activities.

Our Objectives
- To undertake Product Development, Contract Research, and Consultancy.
- To provide technical support to Industries in Application of Electronics and IT
- To develop Entrepreneurs and Designers in Electronics and IT.
- To train manpower in Electronics Product Design, Manufacturing, Maintenance, and IT
- To impart quality IT training through National Level Examination and Certification

Our Vision

To be the leader in the development of industry oriented quality education and training and be the country's premier Institution for examination and certification in the field of Information, Electronics and Communications Technology (IECT).

Long Term Courses

M. Tech in Embedded Systems (since 2005)
M. Tech in Electronics Design Technology (since 2011)

Short Term Courses

PG Diploma in Embedded System Design
PG Diploma in Embedded Real-Time Systems
PG Diploma in Embedded Wireless & Mobile Applications
PG Diploma in Industrial Automation System Design
PG Diploma in VLSI and Embedded Hardware Design
PG Diploma in ASIC Design and Verification
PG Diploma in CAD/CAM
PG Diploma in Software Technology
PG Diploma in Information System Security

PLC/SCADA/DCS Engineer
Advanced course in .NET Technologies
Advanced Course in J2EE

In addition the centre undertakes Faculty training, R&D projects, Industrial consultancy services and implements various schemes of NIELIT in the state of Kerala as well as Karnataka. For facilities and infrastructure, pl. visit "Calicut.nielit.gov.in".

M. Tech Courses in Embedded Systems (ES) & Electronics Design Technology (EDT)

The M.Tech courses in 'Embedded Systems' and 'Electronics Design Technology' are four semester programs. These courses focus on practical design aspects of Embedded Systems and Electronic Product Design and are intended for students who wish to work in the Industry as Design Engineers. The core and elective papers are practical oriented. This program is also an excellent one for those aspiring to take teaching as a profession.

To maximize the engineering, design and problem solving skills of the students and to become more adaptable to real life situations, project work in Industry is very helpful. This not only provides an opportunity to the students to have real life problem solving experience/ project work, but would also help them to apply their class room/ Laboratory knowledge to live situations. To achieve this objective, students are given the option to do the project work at reputed industries.

Objective of the Course

To mould fresh electronics engineers and to upgrade working engineers to High Caliber Embedded System Designers and Electronics Product Designers by enhancing their knowledge and skills in various hardware and software design aspects of Electronic Systems. These courses cover a range of topics of immediate relevance to industry and make the students suitable for working in industries engaged in Embedded System and Electronic Product development. These courses also provide an excellent foundation for those wishing to engage in application research in this rapidly developing area.

Outcome of the Course

- *Participants get exposure to different families and architectures of Embedded System such as Microcontrollers, DSPs, FPGAs, RTOS etc.*
- *Participants will develop the expertise required to design electronic products, embedded system hardware and software using the above tools and become industry ready.*
- *Participants become highly proficient in Embedded Software particularly in real-time programming*

UNIVERSITY AFFILIATION					
	M.Tech courses are affiliated to Kerala Technological University (KTU).				
AICTE APPROVAL					
	M.Tech courses are approved by AICTE and approval for the year 2015-16 is in process.				
DISTRIBUTION OF SEATS					
	M.Tech in Embedded Systems and Electronics Design Technology has 18 seats each. Reservations shall be as per the existing government rules. The seat distribution in each M.Tech is as given below.				
	General (including Physically handicapped and SEBC)	10	SC/ST	3	Sponsored	5

ELIGIBILITY CRITERIA FOR M.TECH COURSES

a. The candidate shall be an Indian National
b. The candidate should have passed the B.Tech course or equivalent from an institution approved by the All India Council for Technical Education.
c. The eligible branches of engineering for each M.Tech are given under the eligibility criteria.
d. Candidates should have a minimum of 60% aggregate marks in the engineering degree examination. For SC/ST candidates, a pass in the engineering degree course is sufficient. For SEBC/PWD candidates, a minimum of 54% (aggregate) marks in the Engineering Degree examination is mandatory.
e. Admission shall normally be restricted to those candidates who have a valid GATE Score.
f. However Non GATE candidates can also apply. They will be considered against the vacancies due to lack of candidates with valid GATE score. These unfilled GATE seats will be filled with NON GATE candidates from Kerala State. They have to produce nativity certificate. If seats are still remaining vacant, candidates from other states will be considered for admission.
g. GATE score is not needed for Sponsored candidates.
h. Candidates who have passed AMIE/AMIETE Examination in Electronics and satisfying the following conditions are also eligible for admission.
   a. They must have a valid GATE score
   b. A Minimum marks of 55% in section B in AMIE/AMIETE Examination
   c. Minimum 3 years of professional experience in the field of specialization after acquiring the qualifying degree.
i. GATE qualified candidates who have appeared for the final examination may also apply provided the results (mark lists/ confidential mark lists) are made available on or before 14-07-2015. Such candidates may be considered for provisional admission. Any candidate admitted provisionally as mentioned above shall have to discontinue the course, if he/she does not produce the final complete mark list and provisional/ degree certificate on or before 31.08.2015.

The eligibility for the two M.Tech courses is as given below.

I. ELIGIBILITY FOR EMBEDDED SYSTEMS (ES)
a. The candidate should have a B.E./B.Tech in Electronics and Communication/ Electrical and Electronics/ Applied Electronics and Instrumentation/ Instrumentation and Control/ Electronics and Instrumentation/ Electronics/ Computer Science/ Computer Science and Engg./ Information Technology/ Biomedical Engg./ Electronics and Biomedical Engg./ Biomedical Instrumentation Engg. subjects approved by the KTU and AICTE.
b. Valid GATE score in Electronics/ Electrical/ Instrumentation/ Computer Science/ IT/ Biomedical engineering subjects.

	II. ELIGIBILITY FOR ELECTRONICS DESIGN TECHNOLOGY (EDT) a. The candidate should have a B.E./B.Tech in Electronics and Communication Engg./ Applied Electronics and Instrumentation Engg./ Electronics and Instrumentation Engg./ Instrumentation and Control Engg./ Electronics and Biomedical Engg. subjects approved by the KTU and AICTE. b. Valid GATE score in Electronics/ Instrumentation/ Biomedical engineering subjects.

SPONSORED SEATS

A sponsored candidate must have at least **three years** of service on the date of application. Their employers must sponsor them and relieve them to pursue the programme for the full duration of the course. GATE score is not needed for sponsored candidates.

Candidates from Central/State Govt. Institutions, Public Sector Organizations and reputed Private Organizations will be considered in this category. Teachers sponsored from AICTE recognized Engineering Colleges will also be considered in this category.

SELECTION OF CANDIDATES

The admission to the course shall be based on the following criteria:

1. For **General and SC/ST** category admission, preference shall be given to candidates who have a valid GATE score.
2. Should have passed the B.Tech/B.E course with a minimum of 60% marks.
3. SC/ST candidates should have minimum pass mark.
4. SEBC/PWD candidates should have a minimum of 54% (aggregate) marks in the Engineering Degree examination.
5. Should have valid GATE Score (see the eligibility criteria for each M.Tech)
6. Any seats unfilled by eligible GATE qualified candidates shall be filled with NON GATE candidates from KERALA state, the selection of which shall be based on marks in the qualifying degree examination. **They should produce nativity certificate**.
7. GATE qualified candidates awaiting results, but who have completed the engineering degree course and passed all subjects and are able to produce the proof of passing with complete mark lists or confidential mark lists from University on or before 14.07.2015 shall be considered for provisional admission. They have to produce the complete mark lists and provisional/degree certificate on or before 31.07.2015. If they fail to meet admission criteria, the fees paid by them will not be refunded in any case.
8. In the case of students with the same GATE score then the selection shall be based on the marks in the Qualifying degree. In case a tie still exits, the selection shall be as follows,

    i. M.Tech ES
    a. Preference shall be with GATE Score in the order as given below: Electronics/ Electrical/ Instrumentation/ Computer Science/ IT/ Biomedical engineering subjects.
    b. In case of a further tie, next preference shall be according to the order of the qualifying degree specified above.

    ii. M.Tech in EDT
    a. Preference shall be with GATE Score in the order as given below: Electronics/ Instrumentation/ Biomedical engineering subjects.
    c. In case of a further tie, next preference shall be according to the order of the qualifying degree specified above.

9. For **Sponsored candidates**, minimum 3 years of experience on the date of application is required and they should produce the necessary certificates such as the Original sponsorship certificate and Original Experience Certificate at the time of counselling/admission. GATE score is not required for sponsored students. Selection of these candidates is based on marks in the qualifying examination and performance in the interview conducted by the selection committee with a weightage of 80:20 respectively.
10. Vacant Sponsored seats shall be filled with eligible candidates from the general list.
11. The counselling/admission will be on **14-07-2015 for GATE qualified candidates** and **16-07-2015 for NON-GATE qualified candidates**. **All are requested to attend the counselling and admission in person** on that day with the original mark lists, certificates and other necessary documents.
12. Admission is subject to satisfying the requirements and availability of seats. Attending the counselling does not guarantee admission.
13. Candidates offered admission will have to remit the one-year fees or at least one-semester fees on the same day of counselling, failing which the admission is not guaranteed.
14. **Transfer Certificate issued from the institution last attended must be produced at the time of admission.**

FEES

Fees shall be Rs 65,000/- per semester which includes the semester tuition fees of Rs 50,000/- and special laboratory tuition fees of Rs 15,000/-. University affiliation, examination and other fees is approximately Rs 8,000/- per year. Refundable caution deposit is Rs 5,000/-. The semester fee is excluding the hostel and food charges.

SC/ST students are eligible for full tuition fee waiver. However they need to pay the refundable caution deposit of Rs 5,000/- and University affiliation fee of Rs 8,000/-. Physically handicapped candidates can avail of 25% fee exemption on the tuition fee, if their annual family income is below Rs 1,00,000/- for which they need to produce income certificate from a Tahsildar. Other university fees shall be extra for students of all categories. Semester exam fees shall be as decided by KTU from time to time.

	Sem1	Sem2	Sem3	Sem4
General & Sponsored	78,000/-	65,000/-	77,000/-	65,000/-
SC/ST	13,000/-	Nil	4,000/-	Nil

FEE PAYMENT MODE

Candidates can pay the semester fees as well as the application processing fee of **Rs 1000.00** for general and **Rs 500.00** for SC/ST candidates either through **Demand Draft (DD) or direct online payment or by using SBI Collect**. DD should be taken in favour of **'Director, NIELIT, Calicut'**, payable at State Bank of India, NIT Calicut Branch (Code No. 02207).

Click here for the **fee payment modes.**

The fees can be paid directly into our account from any Bank where core banking facility is available. The Institute will not be responsible for any mistakes done by either the bank concerned or by the depositor while remitting the amount into our account. The candidate should provide the proof of online payment with the UTR Number/Journal ID from the branch while depositing cash directly into our account. Name of the Student, Date of Payment, Amount Deposited, Name of Bank/branch through which amount deposited, Proof of Deposit (UTR Number/Journal ID) should be provided.

The details required for direct payment are as given below.

1.	Bank Account No	:	31329537747	
2.	Account Type	:	Savings Bank	
3.	Account Name	:	Director NIELIT Calicut	
4.	Bank Name	:	SBI, NITC (CREC) Branch Chathamangalam – 02207	
5.	Bank Address	:	NIT Campus Post, Chathamangalam, Calicut, Kerala - 673601 Ph: 0495 - 2287239	
6.	IFS Code	:	SBIN0002207	
7.	MICR	:	673002012	
8.	Mode of Electronic Transfer	:	RTGS, NEFT, ECS, CBS	

RECOGNITION CERTIFICATE

For candidates with qualifying degree from Universities and Deemed universities outside Kerala, Equivalence certificate from the KTU may be produced if applicable.

SCHOLARSHIP

All GATE qualified candidates are eligible for teaching assistantship (TA) subject to AICTE and MHRD guidelines. This facility will not be available to students admitted under Sponsored Candidates quota, Foreign Nationals and Dependants of NRIs. Disbursement of TA will only be done after the Govt. of India makes funds available. As per MHRD directives, a fellow holding TA shall not accept or hold any appointment paid or otherwise or receive any emoluments, salary, stipend etc from any source during the tenure of the award.

The students joining the programme under this category will be considered for Teaching Assistantships provided by AICTE, based on the following norms:
(a) Students getting assistantship will be required to assist / work for courses, laboratory, or any other related academic / administrative work to the extent of 8 to 10 hours per week related to teaching and research activities as assigned by the institutions.
(b) The students must secure first class or equivalent during the first Semester examination to become eligible for continuance of scholarship.
(c) The student shall give an undertaking to the effect that he/she will not leave the course midway or appear for any competent Examination not related to Engineering and Technology.
(d) The students shall not receive any emoluments, salary, stipend etc from any other source during the course of study.
(e) The assistantship will be available for a maximum period of 24 months and students with TA have to complete M.Tech in two years.
(f) Assistantship will be paid on the basis of monthly attendance.

DISCONTINUING THE COURSE & REFUND OF FEES

1. In the event of a student withdrawing before the starting of the course and closing of admissions, the refund of fees shall be the Entire fee less Rs 1000/- (Rupees One Thousand only) as processing fee.

2. Should a student leave after joining the course and if the seat consequently falling vacant has been filled by another student before the closing of admission, the fee collected will be returned with proportionate deductions of monthly fee and proportionate hostel rent. The monthly fee shall be Rs 10833.00 (semester fees of Rs 65000.00 spread over six months per semester). The hostel rent shall be the yearly hostel fees spread over 12 months. The monthly fees and the hostel rent up to the month the student leaves shall be collected and the balance amount to be returned.

3. If any student admitted for M.Tech discontinues the studies after the closing of admissions in the same academic year or in subsequent academic years, to join other courses or for other purposes, he/she is liable to pay liquidated damages amounting to the fees of all the balance semesters at the rate of Rs. 65,000.00 per semester to NIELIT Calicut. This is not applicable to those who have paid all the fees in which case such fees shall be forfeited towards the liquidated damages. Any candidate admitted provisionally and has to discontinue the course since he/she does not produce the complete mark list and provisional/degree certificate on or before 31.08.2015 will also have to pay the full fees for all the semesters. In all such cases the Transfer Certificate will be issued only after remitting the liquidated damages to the authority concerned. Candidates belonging to SC/ST category shall be exempted from paying the liquidated damages.

4. If any student admitted for M.Tech avails scholarship or stipend (such as GATE scholarship or SC/ST fee reimbursement) etc and discontinues the studies after the closing of admissions in the same academic year or in subsequent academic years, to join other courses or for other purposes, he/she has to pay all the scholarship or stipend received so far back to the authority concerned.

RAGGING CASES

In case the applicant for admission is found to have indulged in ragging in the past his/her admission shall be refused or if it is noticed later that he/she has indulged in ragging in the past or is indulging in ragging during the period of the M.Tech course duration, the concerned student shall be given liberty to explain and if his/her explanation is not found satisfactory, his/her admission gets cancelled and the authority would expel the student from the institution. Further he/she has to pay a liquidated damages of Rs. 65,000.00.

WHO SHOULD APPLY?

Those who are seriously interested in pursuing a design career in Embedded Systems and Electronics Product Design and having an aptitude in programming as well as hardware design only shall apply. It is also expected that the student is good in electronics fundamentals.

HOW TO APPLY?

1. Candidates should **apply on-line**. After filling all the required fields, a print out of the application should be taken and sent to the M.Tech coordinator, NIELIT Calicut along with the appropriate application fees. On-line application link is - http://regn.calicut.nielit.in/
2. In case the on-line mode is not possible, one can use the appropriate application form for admission to M.Tech programme obtained from NIELIT Calicut or downloaded from NIELIT Calicut Website calicut.nielit.gov.in
3. A passport size photograph taken not earlier than 3 months is to be affixed on the Application Form. The photo in the application should be self-attested. Four more copies of the same photograph and one stamp size photograph should be produced at the time of counselling/admission.
4. The following documents should be enclosed along with the application:
   i. Copies of the Mark Lists & Degree Certificate of the Qualifying Examination.
   ii. Course Completion Certificate from the Head of the Institution last studied and attested copies of Mark Lists up to the pre-final semester/ pre-final year for those who have appeared in the qualifying examination and are awaiting results.
   iii. Copy of the GATE Score Card (if applicable).
   iv. Copy of professional experience for AMIE/AMIETE (if applicable).
   v. Copy of Community Certificate in the case of Scheduled Caste/Scheduled Tribe candidates from a competent authority not below the rank of a Tahsildar.
   vi. Copy of Community and Income certificate issued by the Village Officer in the

	case of candidates who claim eligibility under SEBC.
	vii. Copy of Nativity Certificate in the case of NON GATE candidates from Kerala State issued by the Village Officer. viii. Copy of proof of Date of Birth. ix. Copy of Sponsorship Certificate and Experience Certificate in the case of Sponsored Candidates (if applicable). x. Physical disability certificate (if applicable). xi. Proof of payment of application form fee of Rs **1000.00** for general and Rs **500.00** for SC/ST candidates. 5. A single application form with the above amount is to be sufficient for both M.Tech's. Preferences should be clearly mentioned in the application form. 6. The application with all the above enclosures should be sent by Registered Post/Speed Post on or before 5.30 pm of 30-06-2015 so as to reach the **'M.Tech Coordinator, NIELIT, PB No. 5, NIT Campus P.O, Calicut – 673 601 Kerala'**.
COUNSELLING	
	All Sponsored and GATE qualified candidates have to report personally for counselling by **9.00 am on 14-07-2015** with all the necessary documents (originals and attested copies). The interview shall be from 9.30 am onwards. **All Non-GATE candidates** have to report personally for counselling **on 16-07-2015** with all the necessary documents (originals and attested copies). Their admission shall be against the vacancies which are published in the website after the admission on 14th July 2015. Those who fail to submit the application form before 5.00 pm on 30-06-2015and those who do not bring the necessary documents (originals, attested copies and proof of passing) on 14-07-2015 are not eligible for interview or admission. The selected candidates have to pay the full fees or one-semester fees for admission on the same day. The original certificates and mark lists starting from SSLC or equivalent upto the present qualifying degree have to be submitted. Transfer Certificate in original issued from the institution last attended must be produced at the time of admission. The classes shall commence on 03-08-2015. **All candidates awaiting result, who have shown proof of passing on the counselling day and are taking admission, have to pay the full one year academic fees**. Their admission gets cancelled if they are unable to produce the complete mark lists and provisional/degree certificate on or before 31.08.2015 and if they fail to meet admission criteria. The fees paid by them except the caution deposit will not be refunded under any circumstances.
COUNSELLING INTIMATION	
	All those who apply shall have to be present in person for the counselling on 14-07-2015. There shall not be any separate intimation for the counselling.
IMPORTANT DATES	
	Commencement of Issue of Application Forms
	Last Date for Issue of Application Forms
	Last Date for Receipt of Completed Application forms (before 5.00 pm)
	Counselling/Admission for GATE students
	Counselling/Admission for NON-GATE students
	Reporting/Commencement of Classes

IMPORTANT DATES

	Commencement of Issue of Application Forms	07-05-2015
	Last Date for Issue of Application Forms	30-06-2015
	Last Date for Receipt of Completed Application forms (before 5.00 pm)	30-06-2015
	Counselling/Admission for GATE students	14-07-2015
	Counselling/Admission for NON-GATE students	16-07-2015
	Reporting/Commencement of Classes	03-08-2015

TELEPHONE NUMBERS		
	Office	0495 - 2287266 / 2287268
	Director's Office	0495 - 2287123
	Co-ordinator - M.Tech	0495 – 2287266 / 2287177/ 9447541518
	Office Fax	0495 - 2287168
ADDRESS FOR CORRESPONDENCE		
	M.Tech Coordinator, NIELIT, PB # 5, NIT Campus P.O., Calicut – 673601, Kerala, India	
	Ph:	0495 - 2287177
	Fax:	0495 - 2287168
	e-mail:	**mtech@calicut.nielit.in**
	Website:	**calicut.nielit.gov.in**

OFFICE TIMINGS

The office timings are from 9.00 am to 5.30 pm with half an hour Lunch break at 1.00 pm. Saturday and Sunday are holidays.

CLASS & LAB TIMINGS

This program is a practical oriented one, hence there shall be more lab than the theory classes. The classes and labs are from 9.30 am to 12.45 pm and 1.30 pm to 5.30 pm Monday to Friday. During project work, the timings are from 9.15 am to 5.30 pm.

PROJECT WORK

The project work of two semester duration is part of the course and can be done in an Industry or in NIELIT during the third and fourth semesters.

PLACEMENT

We have a placement cell, which provides placement assistance to students who qualify our courses. The Syllabus for this M.Tech is prepared in collaboration with reputed Embedded System Companies in Bangalore and Techno Park Trivandrum such as Wipro, GE Power Controls, Robert Bosch, Philips, Texas Instruments, Analog Devices, Wind River, BBS, MindTree, TATA Elxsi, etc.

The previous years students are placed in companies such as WIPRO, INFOSYS, BOSCH, SIEMENS, MISTRAL, TATA Elxsi, KYOCERA WIRELESS, CAPTRONICS, GDA Tech, HONEYWELL, SANKHYA TECHNOLOGIES, Caterpillar etc.

LAB FACILITIES

The centre is equipped with state-of-the art lab facilities in Embedded Systems in the country. These Include,
- Microcontroller Dev. Systems - Intel 8051, 80C196, MPLAB for PIC 16 & 18 series, ARM9, Cortex M3, ADS, KEIL 'C' IDE,
- Code Composer Studio, Visual DSP, TI & AD DSP Dev.Systems –'C6000, 'C5400, 'C5500, 'C243, 'C2812, 2191, 21065, 21061 (Sharc) DSPs
- VxWorks & RTLinux RTOS
- Xilinx ISE FPGA Design Tools, Leonardo Spectrum, Model Sim Simulator
- Linux for Embedded Applications, Matlab, Simulink, TI 'C6000 target for Matlab,
- LabView, Universal DATA I/O programmer, PC Based EDA tools (ORCAD)
- 500, 350, 300, 100 MHz Digital Storage & Mixed Signal Oscilloscopes
- EMI Test Setup, Wireless LAN, Logic Analyzer, SMD Rework station

HOSTEL/CANTEEN FACILITY

We provide hostels with shared accommodation for both women and men at reasonable rent.
For men, hostel rent ranges from Rs 850/-   to Rs 1300/- per month.
For Women, hostel rent ranges from Rs 1000/-   to Rs 1400/- per month.
A canteen is also functioning inside the hostel and food at reasonable rates is available.

LOCATION & HOW TO REACH?

We are located 20 kms away from Calicut City and is one bus stop before the NITC (REC), Calicut. The bus stop is 'CEDT' or 'Panthrandu' and the present bus fare is Rs 15.00 from city.

SPONSORSHIP CERTIFICATE

(*This should be typed on the letterhead of the Sponsoring Organization and enclosed with application for admission*).

To,
THE DIRECTOR,
NIELIT, CALICUT,
PB No. # 5,
NIT CAMPUS. P.O,
CALICUT, KERALA – 673601

Sub : Sponsoring of an employee for M.Tech Program

Dear Sir,

We hereby Sponsor the candidature of Shri / Smt. / Kum _____ _____ _____, an employee in our organization, for joining his / her M.Tech. Program in Embedded Systems / Electronics Design Technology at your Institute as a fulltime candidate.

We certify that he/she is an employee of our organization for_____ years from _____. We shall fully relieve him/her of his/her duties in the organization during the entire period of the M.Tech program, to enable him / her to devote full time to his / her studies in the Institute.

Address of the Institution   :

Phone number of the   **:**
Institution

Name and signature of the   **:**   Date:
Sponsoring Authority

Designation of the   **:**
Sponsoring Authority

Office Seal

Proforma for Other Backward Class (Non-Creamy Layer) Certificate

(CERTIFICATE TO BE PRODUCED BY OTHER BACKWARD CLASSES APPLYING FOR ADMISSIONS TO CENTRAL EDUCATIONAL INSTITUTIONS (CEIs), UNDER THE GOVERNMENT OF INDIA)

This is to certify that Shri/Smt./Kum. _____ ___
Son/ Daughter of Shri/Smt.
_____ of Village/Town
_____District/Division _____ in the
_____State belongs to the _____ Community
which is recognized as a backward class under:

(i)	Resolution No. 12011/68/93-BCC(C) dated 10/09/93 published in the Gazette of India Extraordinary Part I Section I No. 186 dated 13/09/93.
(ii)	Resolution No. 12011/9/94-BCC dated 19/10/94 published in the Gazette of India Extraordinary Part I Section I No. 163 dated 20/10/94.
(iii)	Resolution No. 12011/7/95-BCC dated 24/05/95 published in the Gazette of India Extraordinary Part I Section I No. 88 dated 25/05/95.
(iv)	Resolution No. 12011/96/94-BCC dated 9/03/96.
(v)	Resolution No. 12011/44/96-BCC dated 6/12/96 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 11/12/96.
(vi)	Resolution No. 12011/13/97-BCC dated 03/12/97.
(vii)	Resolution No. 12011/99/94-BCC dated 11/12/97.
(viii)	Resolution No. 12011/68/98-BCC dated 27/10/99.
(ix)	Resolution No. 12011/88/98-BCC dated 6/12/99 published in the Gazette of India Extraordinary Part I Section I No. 270 dated 06/12/99.
(x)	Resolution No. 12011/36/99-BCC dated 04/04/2000 published in the Gazette of India Extraordinary Part I Section I No. 71 dated 04/04/2000.
(xi)	Resolution No. 12011/44/99-BCC dated 21/09/2000 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 21/09/2000.
(xii)	Resolution No. 12015/9/2000-BCC dated 06/09/2001.
(xiii)	Resolution No. 12011/1/2001-BCC dated 19/06/2003.
(xiv)	Resolution No. 12011/4/2002-BCC dated 13/01/2004.
(xv)	Resolution No. 12011/9/2004-BCC dated 16/01/2006 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 16/01/2006.

Shri/Smt./Kum. _____and/or his family ordinarily reside(s) in the
_____ District/Division of _____ State. This is also to
certify
that he/she does not belong to the persons/sections (Creamy Layer) mentioned in Column 3 of the Schedule to the Government of India, Department of Personnel & Training O.M. No. 36012/22/93-Estt.(SCT) dated 08/09/93, modified vide OM No. 36033/3/2004 Estt.(Res.) dated 09/03/2004, or the latest notification of the Government of India.

This certificate is being issued based on the annual income / status of the parents / guardian of the applicant as on financial year ending March 31, 2015.

Dated: District Magistrate/ Deputy Commissioner, etc.

                                                Seal
NOTE:
(a) The term 'Ordinarily' used here will have the same meaning as in Section 20 of the Representation of the People Act, 1950.
(b) The authorities competent to issue Caste Certificates are indicated below:
(i) District Magistrate / Additional Magistrate / Collector / Deputy Commissioner / Additional Deputy Commissioner / Deputy Collector /Ist Class Stipendiary Magistrate / Sub-Divisional Magistrate / Taluka Magistrate / Executive Magistrate / Extra Assistant Commissioner (not below the rank of 1st Class Stipendiary Magistrate).
(ii) Chief Presidency Magistrate / Additional Chief Presidency Magistrate / Presidency Magistrate.
(iii) Revenue Officer not below the rank of Tehsildar and
(iv) Sub-Divisional Officer of the area where the candidate and / or his/her family resides.
The date of issue of OBC (NCL) certificate should be after 31 March, 2015

Documents to be enclosed along with the application:

xii. Copies of the Mark Lists & Degree Certificate of the Qualifying Examination.
xiii. Course Completion Certificate from the Head of the Institution last studied and attested copies of Mark Lists up to the pre-final semester/ pre-final year for those who have appeared in the qualifying examination and are awaiting results.
xiv. Copy of the GATE Score Card (if applicable).
xv. Copy of Community Certificate in the case of Scheduled Caste/Scheduled Tribe candidates from a competent authority not below the rank of a Tahsildar.
xvi. Copy of Community and non creamy layer Certificate in the case of SEBC candidates from a competent authority not below the rank of a Tahsildar.
xvii. Copy of Nativity Certificate in the case of NON GATE candidates from Kerala State from competent authority.
xviii. Copy of proof of Date of Birth.
xix. Copy of Sponsorship Certificate and Experience Certificate in the case of Sponsored Candidates.
xx. Proof of payment of application form fee of **Rs 1000.00** for general and **Rs 500.00** for SC/ST candidates.

The application with all the above enclosures and superscribed **'Application for M.Tech Courses - 2015'** should be sent by Registered Post/Speed Post on or before 5.00 pm of 30–06-2015 to the

'M.Tech - Coordinator,
NIELIT,
PB No. 5,
NITC Campus P.O,
Calicut – 673 601
Kerala'.

Previous Year (2014-15) GATE CUT OFF SCORE

	ES Highest	ES Lowest	EDT Highest	EDT Lowest
2014 - General	642	481	554	415
2014 – SC/ST	364	330	346	272