

राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान (रा.इ.सू.प्रौ.सं.)
National Institute of Electronics and Information Technology (NIELIT)
(An ISO 9001: 2015 Certified Organisation)

इलेक्ट्रॉनिकी और सूचना प्रौद्योगिकी मंत्रालय, भारत सरकार
Ministry of Electronics and Information Technology, Government of India

Ref. No. NHQ-11011/4/2025-NIELIT /E-3153769/886

December 26, 2025


OFFICE ORDER

With the approval of the Competent Authority, the following officers, presently posted at NIELIT Ropar Centre, are hereby promoted under Modified Person Oriented Promotion Policy of NIELIT with immediate effect or from the date of assumption of charge to the next higher level, whichever is later:

S. No.	Name, Present Designation & Place of Posting	Present Pay Level	Pay level after promotion
1	Ms. Jyoti Sharma P.T.O, NIELIT Ropar/Chandigarh	Level -11	Level-12
2	Ms. Jayati Puri P.T.O, NIELIT Ropar/Chandigarh	Level- 11	Level-12

2. The pay of the above-mentioned officers in the promoted level shall be fixed as per Govt. of India Rules, as applicable to NIELIT.

3. The above-mentioned officers will continue to work at their present place of posting, however, they are liable to be transferred to any of the NIELIT Centres, as and when required.


(Dr. S.K. Dhurandher)
Registrar

To:

i) Officer Concerned: *With a request to exercise the option for fixation of pay as per GoI Orders under FR 22 (I)(a)(1) within one month of assumption of charge and convey the same to Finance & Personnel Section of the respective Centres. Option once exercised will be final.*

ii) Executive Director/Director/Director-in-Charge, all NIELIT Centres

iii) IT&IS Wing: *For uploading the order on the NIELIT website*

iv) Official Language Section, NIELIT HQs: *with a request to issue Hindi version of the above order*

Copy for kind information to:

- i) Director General, NIELIT
- ii) Chief Vigilance Officer, NIELIT



इलेक्ट्रॉनिकी और सूचना प्रौद्योगिकी मंत्रालय, भारत सरकार की एक स्वायत्त वैज्ञानिक संस्था
An Autonomous Scientific Society of Ministry of Electronics and Information Technology, Government of India
नाइलिट भवन, प्लॉट नं. 3, पीएसपी पॉकेट, इंस्टीट्यूशनल एरिया, सेक्टर-8, द्वारका, नई दिल्ली - 110077
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