राष्ट्रीयइलेक्ट्रॉनिकीएवंसूचनाप्रौद्योगिकीसंस्थान,कालीकट
National Institute of Electronics & Information Technology, Calicut

Ministry of Electronics and Information Technology
Government of India

Internship Training
Prospectus

ISO 9001-2015 Certified

Digital Design using Verilog HDL

28th June to 02nd July 2021
(1Week-LMS based flexible learning with lab)

Course Description

The objectives of the course is to:

1. **Provide a thorough understanding about and hands-on with digital design & Test bench based verification using Verilog HDL.**

2. **Provide a thorough understanding and hands-on practice, needed to independently take up projects, involving Digital Logic Design.**

Click Here to Register

Who can attend?

Anyone who is interested in learning RTL Design & verification of Digital Circuits & Systems.
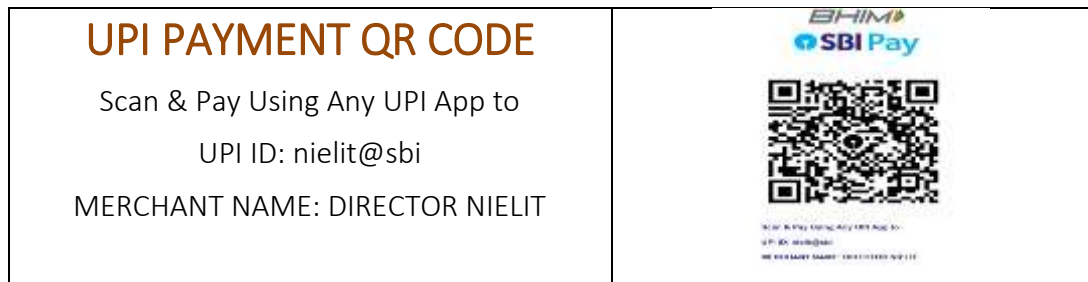
- Students pursuing Engineering can gain the best from the program.

Course Content

Sno	Topics
1.	Introduction to Verilog HDL (IEEE 1364-2005) & Hierarchical Modeling Concepts
2.	Lexical Conventions & Data Types
3.	Modules, Ports and Module Instantiation Methods
4.	Gate Level & Data Flow Modeling
5.	Behavioral Modeling
6.	Algorithmic Modeling
7.	Design Verification using Test benches
8.	Mini Project /Case study

Fee and important dates

Fee	Rs. 2000/- *(Including GST)*
Last date for registration & payment	21st June 2021

Certificate

e-Certificate to all completed candidates

Minimum System Requirements

- Attendees must have a computer system with a good internet connection.
- Computer System Configuration (any one) – Windows OS 7/8/10, i3/i5, 64bit processor, Minimum 4GB RAM

Faculty Profile

Team of Engineers from NIELIT Calicut with 12+ years' experience

Course Coordinator

Nandakumar.R
Scientist /Engineer 'D'
Ph # 9995427802(Whatsapp)
Email:nanda24x7@gmail.com

Terms and Conditions

1. In case any registered candidate could not attend the offline/ online session due to technical   issue at their side there will not be any refund of the course fee and the sessions will   not be repeated.
2. In case the offline/ online course is cancelled /postponed due to some technical issue at   NIELIT side and new date is not convenient to the candidate, our liability is limited to the refund of the course fee and NIELIT shall not be responsible for any consequential damages.

For more details about our institution and facilities visit us

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Autonomous Scientific Society of
Ministry of Electronics & Information Technology,
Govt. of India

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