

SCHEDULE
Advt. No.: No. NHQ-12/12/2025-NC (33158412)

Date	S.No.	Name of Position	Position Code	Schedule	
16-06-2025 (Monday)	1.	Team Lead (VLSI Design)	2506-TLVD	Document Verification	9:30 AM - 12:00 PM
	2.	VLSI Design Expert	2506-VLSIDE		
	3.	Senior Trainers (VLSI Design)	2506-STVD		
	4.	Team Leader (Platform Development Team)	2506-TLPDT		
	5.	DevOps Engineer	2506-DOE		
	6.	Full Stack Developer	2506-FSE	Interview	11:00 AM -05-00 PM
	7.	Graphic Designer	2506-GD		
	8.	Sr. Resource Person	2506-SRP		
	9.	Resource Person (Admin & Account)	2506-RPACC		
	10.	Assistant	2506-ASSTT		

Date	S.No.	Name of Position	Position Code	Schedule	
17-06-2025 (Tuesday)	11.	Senior Resource Person (Project Officer)	2506-SRPPO	Document Verification	9:30 AM - 12:00 PM
	12.	Junior Resource Person (Project Associate)	2506-JRPPA		
	13.	Resource Person	2506-RP		
	14.	Senior Resource Person (Information Technology)	2506-SRPIT		
	15.	Consultant	2506-CONSUL	Interview	11:00 AM -05-00 PM
	16.	Senior Resource Person	2506-SRPCER		
	17.	Junior Resource Person (Support)	2506-JRPSCE		

Date	S.No.	Name of Position	Position Code	Schedule	
18-06-2025 (Wednesday)	18.	Senior Resource Person (Fin & Acc)	2506-SRPFADM	Document Verification	9:30 AM - 12:00 PM
	19.	Junior Resource Person (Facility Management)	2506-JRPFMG		
	20.	Faculty (Computer Science & Engineering)	2506-FACCSE		
	21.	Assistant Professor (Electronics)	2506-APELEC		
	22.	Assistant Professor (Computer Science & Engineering)	2506-APCSEG		
	23.	Consultant	2506-CONSULIC	Interview	11:00 AM -05-00 PM
	24.	Consultant	2506-CONSULPR		

Venue for Document Verification and Interview

National Institute of Electronics & Information Technology
NIELIT Bhawan, Plot No. 3, PSP Pocket, Sector-8, Dwarka, New Delhi-110077

Note: All applicants are requested to refer General Terms & Condition as mentioned in detailed Advertisement.