रा.इ.सू.प्रौ.सं NIELIT	**National Institute of Electronics and Information Technology** **NIELIT Bhawan, Plot No. 3, PSP Pocket, Sector-8,** **Dwarka, New Delhi-110077**

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NIELIT Invites Applications from suitable, interested and eligible candidates for the following position(s):- Assistant Professor (ECE/EE/CSE and Allied branches), Associate Professor (ECE//CSE and Allied branches), Professor (ECE//CSE and Allied branches), Team Lead (VLSI Design), VLSI Design Expert/ Junior VLSI Engineer, Senior Trainers (VLSI Design)/ Senior VLSI Engineer, Team Leader (Platform Development Team), DevOps Engineer, Full Stack Engineer, Graphics Designer, Sr. Resource Person, Consultant (Design Verification & EDA Integration), Senior Resource Person (Project Officer), Resource Person, Consultant, - purely on temporary and short-term contract basis on consolidated remuneration initially for a period of one year which may be considered for extension based on performance and requirement. Eligible candidates can apply by submitting offline applications along with the required documents as per details specified in the Detailed Advertisement published and should reach NIELIT HQs on or before **25th July 2025**. Applications received after the due date in NIELIT shall summarily be rejected. For detailed advt. with eligibility criteria, remuneration, age, terms and conditions and application proforma log on to https://nielit.gov.in/recruitments

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