

राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान, चिप डिज़ाइन उत्कृष्टता केंद्र, नोएडा

DETAILED ADVERTISEMENT  
(Advt. No. NIELIT/NOIDA/CoE/2026/04)

NIELIT CoE Noida requires the following person purely on contract basis on consolidated remuneration initially for a period of one year: -

<b>1. Name of the Position</b>	<b>VLSI Design Expert/ Junior VLSI Engineer</b>
<b>No. of Position</b>	<b>02</b>
<b>Tenure/ Period</b>	1 year (purely on contract basis)
<b>Age Limit</b>	Up to 45 Years
<b>Emoluments (Monthly) (inclusive of statutory compliances, if any) [CTC]</b>	Rs. 40,000/- P.M
<b>Date of walk-in interview</b>	25/Feb/2026
<b>Eligibility Criteria and Experience</b>	<p>Master's in Electronics &amp; Communication, Electrical Engineering, VLSI Design, or related field. <b>OR</b> Bachelor in Electronics &amp; Communication, Electrical Engineering, VLSI Design, or related field with <b>Two year's experience</b></p> <p><b>Job description:</b> Key responsibilities include:</p> <ul style="list-style-type: none"><li>• To assist in design and development of reusable IP blocks using open-source EDA tools, their validation and documentation</li><li>• Design and develop in-depth academic and practical content in one or more of the following domains:<ul style="list-style-type: none"><li>• <b>RTL Design &amp; Integration</b></li><li>• <b>Physical Design</b></li><li>• <b>Synthesis and STA</b></li><li>• <b>DFT and ATPG</b></li><li>• <b>Analog and Mixed-Signal Design</b></li><li>• <b>Digital and Analog Design Verification</b></li><li>• <b>Standard Cell and PDK-based design</b></li></ul></li><li>• Create reproducible design labs and projects using open- source tools such as:<ul style="list-style-type: none"><li>• <b>Magic, KLayout, Yosys, OpenROAD, Ngspice, Xschem, Netgen, GHDL, Verilator</b>, etc.</li></ul></li><li>• Develop and document exercises, assignments, and mini- projects for each design domain.</li><li>• Record demos or work with AV teams to create training videos and tutorials.</li><li>• Collaborate with content designers, platform developers, and trainers to align content with platform capabilities.</li><li>• Test and validate open-source flows with actual design examples and student-level reproducibility.</li><li>• Mentor junior engineers, interns, and trainers when needed.</li></ul> <p><b>Preferred Skills</b></p> <ul style="list-style-type: none"><li>• Prior teaching, curriculum development, or instructional design experience.</li><li>• Exposure to cloud-based VLSI design environments or FPGA-based learning kits.</li><li>• Knowledge of modern teaching aids – LMS platforms (e.g., Moodle), video content creation, screen recording tools.</li><li>• Strong written and verbal communication skills.</li></ul>

### General Terms & Conditions:

1. The qualification of candidates must be from Govt. University or Govt. recognized University/Institutions.
2. Applicants with last semester result awaited or incomplete degrees are not eligible to apply.
3. Cut-off date for calculating age and experience shall be the date mentioned in Walk-in-Interview Advertisement.
4. Non-refundable Registration Fee of ₹500/- per position to be deposited through online mode. The Candidates may deposit registration fee in the account of NIELIT Gorakhpur in the given Bank account on or before Document Verification & Walk-in-Interview date.

<b>Name of the office</b>	NIELIT Gorakhpur Centre
<b>Bank Account Number</b>	1914002100094252
<b>Bank Name</b>	Punjab National Bank
<b>IFSC Code</b>	PUNB0191400

<b>Venue for Document Verification and Interview:</b>	NIELIT COE Noida, PS-1D, Behind Brahmputra Shopping Complex, Sector-29, Noida, 201303
<b>Document Verification</b>	09:30 AM to 11:30 AM
<b>Interview date and Time</b>	25/Feb/2026; 11:00 AM onwards

5. Applications without requisite application fee (Rs. 500/-) shall be summarily rejected at any stage of Document Verification and Walk-in-Interview.
6. Applicants are requested to fill Application Form and attach all the required (self-certified) documents including Payment Receipt.
7. The Applicants who report at NIELIT COE Noida before 02:00 PM shall be considered for Document Verification. Document verification after 2:00 pm may be considered subjected to the approval of competent authority
8. Applicants are requested to come with Original Documents for Document Verification. Document Verification (based on the documents produce by applicant with application form) and Walk-in Interview is to be done as per date mentioned in the advertisement.
9. Only those candidates who clears Document Verification will be allowed to appear in Walk-in-Interview.
10. Applicants are requested not to send Application Form through Post or any other medium to NIELIT CoE Noida.
11. NIELIT CoE Noida has the right to accept or reject the application without assigning any reason thereof.
12. Applicants are advised to visit the website of NIELIT CoE Noida <http://coenoida.in> and NIELIT Gorakhpur website <http://nielit.gov.in/gorakhpur/recruitments> for any updates.
13. No separate communication shall be made in any other form.
14. The number of vacancy is tentative and liable to change as per the requirement of NIELIT CoE Noida.
15. The Remuneration mentioned above is consolidated salary (CTC). The selected candidate will not be paid any other financial benefits like Medical, HRA, Transport etc. except the consolidated salary.
16. Selection of candidate for appointment to the above mentioned position will be based on the performance of the candidates in the walk-in-interview and as found eligible as per prescribed criteria.
17. The selected candidate will be engaged purely on contract basis initially for a period of one year, which may be extended depending upon the requirement and performance of the candidate.
18. The offer of appointment for the selected candidate will be subject to verification of original certificates/testimonials at the time of interview and completeness of other formalities.
19. NIELIT CoE Noida will also create a panel for contractual deployment and the candidates will be selected for deployment on contract from the panel as and when required.
20. Only those candidates who deposit requisite registration fee of Rs. 500/-, clear document verification and successful interaction will be empaneled.
21. Empanelment means the shortlisted candidates will be in the panel of NIELIT CoE Noida for one year. They may be deployed in specific Govt. department/ NIELIT CoE Noida as per department requirements purely on contract basis. Mere empanelment does not ensure deployment in any department nor provide any right to candidate to claim for deployment.
22. Candidates will not be entitled to claim any TA/DA for appearing in Interview.
23. Canvassing/trying to influence NIELIT CoE Noida employees to secure the job in any manner shall disqualify the candidate.



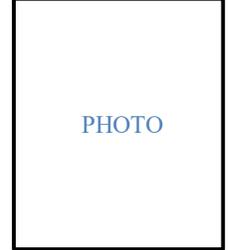
**National Institute of Electronics and Information Technology**  
**Centre of Excellence in Chip Design, Noida**



PS-1D, Behind Brahma Putra Shopping Complex, Sector-29, Noida, 201303

**APPLICATION FORM**

**POST APPLIED FOR** : \_\_\_\_\_



Details of Application Fees: Rs. 500/-

UPI/UTR/Transaction No: \_\_\_\_\_ Date \_\_\_\_\_  
(Payment Receipt Form to be duly attached with this form)

1. Name of the : \_\_\_\_\_  
Candidate
2. Father's Name : \_\_\_\_\_
3. Date of Birth : \_\_\_\_\_
4. Category : \_\_\_\_\_
5. Aadhar Number : \_\_\_\_\_
6. PAN No. : \_\_\_\_\_
7. Correspondance : \_\_\_\_\_  
Address \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_
8. Permanent Address : \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_
9. Contact No. : \_\_\_\_\_
10. E-mail : \_\_\_\_\_

**11. Educational Qualifications 10<sup>th</sup> Class onwards:**

S.No.	Qualification/Degree/Diploma	Name of University/ Institution	Year of Passing	%age of Marks
1.				
2.				
3.				
4.				
5.				
6.				
7.				

**12. Details of Professional Qualifications:**

S.No.	Qualification/Degree/Diploma	Name of University/ Institution	Year of Passing	%age of Marks
1.				
2.				
3.				
4.				
5.				

**13. Details of Experience:**

S.No.	Place of Working	Duration	Period		Brief Description of Duties
			From	To	

**Total Experience in years & months** \_\_\_\_\_

**Documents to be attached:**

- i) Class Xth marksheet
- ii) Class XIIth marksheet
- iii) Qualifying Degree/Pursuing Marksheet
- iv) Self-attested experience certificates (including the Experience letter from the current place of working)
- v) Screenshot/Receipt Copy of the Fee paid to NIELIT
- vi) Affidavit (required in case of gap in studies or gap in employment/work experience)
- vii) Aadhar Card

**Undertaking:**

1. I have gone through the details provided in the website and shall abide by the same.
2. Certified that all the information furnished above by me is true and correct to the best of my knowledge and belief.
3. I fulfill all the condition of eligibility prescribed for the post applied for .
4. I have never been debarred by any organization for any illegal activity during my education/service.
5. I understand that in the event of any information found false /incorrect/suppressed or any ineligibility being detected before or after the test/interview/selection, my candidature is liable to be cancelled / my services are liable to be terminated and no correspondence will be entertained by NIELIT CoE Noida in this regard.

**Place:**

**Date:**

**Signature of Candidate**