



**National Institute of Electronics and Information Technology
NIELIT Bhawan, Plot No. 3, PSP Pocket, Sector-8,
Dwarka, New Delhi-110077**

WALK-IN-INTERVIEW [DATE- 16.01.2026]

NIELIT Invites Applications from suitable, interested and eligible candidates for the following position(s): - **Resource Person** (Academic-Operations), **Consultant** (DFT and FPGA Validation), **Consultant** (Physical Design), **Team Lead** (VLSI Design), **VLSI Design** Expert/ Junior VLSI Engineer, **Senior Trainers** (VLSI Design)/ Senior VLSI Engineer, **DevOps Engineer**, **Full Stack Engineer**, **Resource Person** (Admin and Account), **Assistant**, **LMS Developer** purely on temporary and short-term contract basis on consolidated remuneration initially for a period of six month [for consultant] and one year [for others] which may be considered for extension based on performance and requirement.

Interested candidates may bring completely filled Application Form alongwith photograph and self attested Certificate in support of qualification & experience as per the date mentioned above. Candidate has to be deposit the non refundable registration fee of Rs. 500/- per position through online mode on or before wal-in-interview date. For detailed advt. with eligibility criteria, remuneration, age, terms and conditions and application pro-forma log on to <https://nielit.gov.in/recruitments>

Advt. No. NHQ-12/412025-NIELIT/3169108

Registrar