

National Institute of Electronics and Information Technology (NIELIT)
NIELIT Bhawan, Plot No. 3, PSP Pocket, Sector-8,
Dwarka, New Delhi - 110077

Date: 14/01/2026

Public Notice regarding the Reschedule of O Level Jan'2026 Examination from 10-Jan-2026, Batch-1 & Batch-2 to 17-Jan-2026, Batch-1 & Batch-2 at UPVNS2 (Computer Career Institute, P-20, JP Bhawan, Vrindavan Road, Awaleshpur, Varanasi - 221106, Uttar Pradesh)

1. It is hereby informed to all concerned candidates that, due to administrative reasons, the examination of candidates scheduled on 10 January 2026 (Batch-1 & Batch-2) at UPVNS2 (Computer Career Institute, P-20, JP Bhawan, Vrindavan Road, Awaleshpur, Varanasi - 221106, Uttar Pradesh) has been rescheduled to 17 January 2026, Batch-1 & Batch-2 respectively.
2. All candidates who had **already appeared in the examination on 10 January 2026 in Batch-1 & Batch-2 at the said centre shall be treated as null and void**, and their respective exam papers shall not be evaluated. However, the **examination conducted on the rescheduled date i.e. 17 Jan 2026 shall be treated as valid**.
3. All concerned candidates are hereby requested **to download their revised Admit Cards and appear in their examination as per the rescheduled date i.e. 17 January 2026, Batch-1 & Batch-2**.