

राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान (रा.इ.सू.प्रौ.सं.), कालिकट

**NATIONAL INSTITUTE OF ELECTRONICS AND
INFORMATION TECHNOLOGY (NIELIT), CALICUT**

(An Autonomous Scientific Society of Ministry of Electronics & Information Technology, Government of India)

NIT Campus PO, CALICUT - 673 601

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National Institute of Electronics and Information Technology (NIELIT) Calicut is a premier organization engaged in Education, Training, R&D and Consultancy in the areas of Electronics, IT and ITES. We conduct courses both in the formal (M.Techs) and Non-formal (PG Diplomas and customized courses etc.) sectors. We require the following Project staff on contract basis.

RESEARCH ASSOCIATE – 2 POSTS

PROJECT ENGINEER – 2 POSTS

PROJECT ASSOCIATE – 1 POST

TOTAL POSTS	5 NOS. (3 IN VLSI & FPGA DOMAIN, 2 IN EMBEDDED SOFTWARE DOMAIN)
Project Details	1. Study of AFE processor and hardware accelerated near field beam former implementation on FPGA for imaging SONAR 2. Development of scheduling algorithm under Linux/RTLinux platform for Multi-core Architectures 3. Special Manpower Development Programme-Chip to System Design (SMDP- C2SD)
Qualification	Essential: M.E/M.Tech, B.E/ B.Tech in Electronics/ Electronics and Communication/ Bio-Medical Engineering/ Medical Electronics/ Electronics and Instrumentation/ Computer Science and allied branches/MSc in Electronics. <i>With minimum 2 years of post qualification experience in case of "Research Associate" position.</i>
Requirements	1. VLSI & FPGA Domain: FPGA based Digital Signal Processing System Design, Board Bring up, Board Support Package and Test Jig Development. FPGA implementation of Signal Processing Algorithms, RTOS porting and application software development, Driver bring up and GUI development, System Integration and Testing. Domain knowledge in Digital Signal and Image processing. 2. Embedded Software Domain: Knowledge of Embedded Linux/RT Linux, OS Scheduler algorithm development, Testing, Debugging, Optimization, RT Linux porting, Device Driver development, Signal processing Algorithms development, Testing, Benchmarking, Debugging, Profiling knowledge, Optimization and knowledge of using standard libraries like Intel IPP, Customization of Linux/RT Linux Kernel, Knowledge of Embedded Hardware architectures.

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Preferred Skills	1 & 3. For 3 posts (VLSI & FPGA Domain): Experience in Electronic System Design and manufacturing, RTL Coding using Verilog /VHDL, FPGA Architecture knowledge, Porting Signal processing algorithms on FPGA, Knowledge in LVDS, PCIe protocol. 2. For 2 posts (Embedded Software domain): Strong knowledge in C/C++, Linux, Embedded Os & RTOS, RTLinux, Device driver development, Linux Kernel Module programming, Interrupt handling, Multithreading, Embedded Linux Porting, Embedded Architecture knowledge, Development of porting signal processing algorithms, Knowledge in Embedded bus protocols.
Age	Up to 35 Years
Selection Process	Candidates will be selected based on their performance in test/interview.
Duration of engagement	10 months
Remuneration	₹40000/- per month (consolidated) for Research Associate, ₹25000/- per month (consolidated) for Project Engineer and Upto ₹22000/- per month (consolidated) for Project Associate based on qualification, experience and performance in the Interview.
	No TA/DA for attending the Walk-in-Interview

Interested candidates may appear for a Walk-in-Interview at our Institute on **12th April 2019** at **09.00 AM sharp**.

Candidates should bring with them all **original documents** in support of their age, qualifications, experience, Identity and address proof etc. along with a copy of **signed Bio-data**. Candidates who have the relevant qualification required for the above post **only** need to appear for the selection.

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