

RECRUITMENT NOTIFICATION No. 3(133)/97/Jan 22/01
Last date for submitting the application is 31 Jan 2022

National Institute of Electronics and Information Technology (NIELIT) Calicut is a premier organization engaged in Education, Training, R&D and Consultancy in the areas of Electronics, IT and ITES. We conduct courses both in the formal (M. Tech) and Non-formal (PGDiplomas and customized courses etc.) sectors. We also conduct online proctored examination and Recruitment Activity.

National Institute of Electronics & Information Technology (NIELIT) Calicut, invites applications for the post of Lab Engineer post purely on contract basis for the project:

“SPECIAL MANPOWER DEVELOPMENT PROGRAMME FOR CHIPS TO START-UP”
(SMDP C2S)

Initiated by

Ministry of Electronics and Information Technology, Government of India
to make our country a significant player in VLSI Design and self reliance in Semi Conductor Technologies

Details	Salary per Month	Qualification Essential	Desirable	Age Limit	Desirable Domain Experience
LAB ENGINEER (VLSI & Embedded Systems) Number of Posts – 2	Rs.35,000/- Consolidated	B.E/ B.Tech in Electronics/ ECE/EEE/CSE and allied branches.	M.E/M.Tech. in ECE/ EEE/CSE Or PG Diploma in VLSI/Embedded System	Below 35 Years	Good knowledge in Hardware Design & Verification using Verilog HDL/System Verilog, FPGA based SoC/Embedded System Design. Strong fundamentals in Digital Electronics, Computer Architecture etc. Aptitude to learn and contribute towards development top notch professional quality content for Instruction Enhancement Programs
Scope of Work	Setting up of Industry Standard EDA Tool Chains, High End FPGA Development setups, Remote Hardware Lab Provisioning, Development of curated e-learning contents for Professional Training Programs, Interactions with other SMDP Organizations, and dissemination of the information at the SMDP-C2S website, Contribution to R&D and Startup Hand-holding activities etc.				
Duration of Engagement	Initially for One Year , Extendable for the entire duration of the project, based on the performance.				
Selection Process	Short-listed/Screened-in candidates will be selected based on their performance in test/Interview.				

General Instructions

- Application should be submitted only online through Link: <https://forms.gle/dmXwiXmpvwK3Qwx7A>
- Only Indian nationals are eligible to apply.
- Canvassing in any form will be a disqualification.
- No interim enquiry / correspondence, whatsoever, will be entertained regarding the recruitment.
- Incomplete applications are liable to be summarily rejected.
- NIELIT Centre Calicut reserves the right to fill or not to fill the post(s) advertised.
- There will be no application fee required to be paid.
- Candidates with relevant experience will be preferred. However, the same may be relaxed in deserving cases.
- The vacancies notified are tentative and are subject to change.
- Experience is counted only for the period after obtaining the eligibility qualification.
- Applications, which are not in conformity with the requirements indicated are liable to be rejected.
- The Institute will have the right to fix criteria for screening the applications so as to reduce the number of candidates to be called for interview.
- Only short-listed candidates will be called for Written Test/interview. Mere fulfilling of requirements as laid down in the advertisement does not qualify a candidate for interview.
- No TA/DA will be provided to attend the interview.
- Applicant should have a valid/Active G-mail account for submitting application.
- Hostel facility will be provided to selected candidates on payment basis as per availability.
- Candidate will be rejected, if the information provided by the candidate is found incorrect/failed to produce documentary proof on qualification etc.
- It is the responsibility of the candidate to obtain NOC etc from the current employer/institution, in case already bound by any contract.